

Features

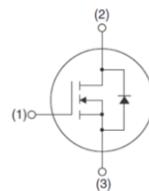
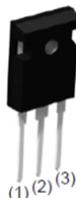
- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Avalanche Ruggedness

Product Summary

V _{DS}	650V
R _{DS(on)_typ}	60mΩ
I _D	37A

Applications

- Solar Inverters
- Switch Mode Power Supplies
- High Voltage DC-DC Converters
- Battery Chargers



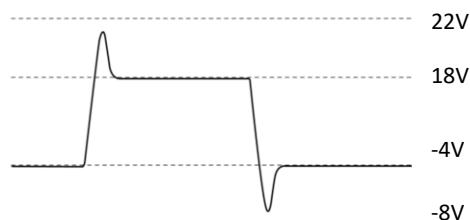
Package Marking and Ordering Information

Part #	Marking	Package
T1M60065D	1M60065D	TO-247-3

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V _{DS}	650	V
Continuous drain current	I _D	37	A
V _{GS} =18V T _C = 25°C		26	
V _{GS} =18V T _C = 100°C			
Pulsed drain current (T _C = 25°C, t _p limited by T _{jmax})	I _D pulse	93	A
Avalanche energy, single pulse (L=10mH)	E _{AS}	590	mJ
Gate-Source voltage	V _{GSOP}	-4/+18	V
Gate-Source voltage (dynamic, Absolute maximum values)	V _{GSmax}	-8/+22	V
Power dissipation (T _C = 25°C)	P _{tot}	183	W
Operating junction and storage temperature	T _j , T _{stg}	-55...+175	°C

- Example of acceptable V_{GS} waveform



Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	R_{thJC}	0.82	°C/W
Thermal resistance, junction – ambient. Max	R_{thJA}	40	

Electrical Characteristic (at $T_j = 25$ °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	BV_{DSS}	650	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Gate threshold voltage	$V_{GS(th)}$	2	-	4	V	$V_{DS}=V_{GS}, I_D=3.6mA$
Zero gate voltage drain current	I_{DSS}	-	1	100	μA	$V_{DS}=650V, V_{GS}=0V$
		-	10	-		$T_j=25^\circ C$
Gate-source leakage current	I_{GSS}	-		250	nA	$V_{GS}=18V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	60	80	$m\Omega$	$V_{GS}=18V, I_D=13.2A,$
		-	73	-		$T_j=25^\circ C$
		-				$T_j=175^\circ C$
Transconductance	g_{fs}	-	9	-	S	$V_{DS}=20V, I_D=13.2A$

Dynamic Characteristic

Input Capacitance	C_{iss}	-	1039	-	pF	$V_{DS} = 650V$ $V_{GS} = 0V$ $T_j = 25^\circ C$ $V_{AC} = 25mV$ $f = 1MHz$
Output Capacitance	C_{oss}	-	90	-		
Reverse Transfer Capacitance	C_{rss}	-	11	-		
Gate Total Charge	Q_G	-	48.9	-	nC	$V_{DS} = 400V$ $V_{GS} = 0/18V$ $I_D = 13.2A$
Gate-Source charge	Q_{gs}	-	11.6	-		
Gate-Drain charge	Q_{gd}	-	20.6	-		
Turn-On Switching Energy	E_{ON}	-	42.4	-	μJ	$V_{DD} = 400V$ $V_{GS} = -4/+18V$ $I_D = 13.2A$ $R_G = 5\Omega$ $L = 100\mu H$
Turn-Off Switching Energy	E_{OFF}	-	16.5	-		
Turn-on delay time	$t_{d(on)}$	-	9.3	-		
Rise time	t_r	-	3.2	-	ns	
Turn-off delay time	$t_{d(off)}$	-	15.7	-		
Fall time	t_f	-	7.4	-		
Gate resistance	R_G	-	0.9	-	Ω	$V_{AC} = 25mV, f=1MHz$

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}		3.7		V	$V_{GS}=0V, I_{SD}=6.6A, T_j=25^{\circ}C$
			3.3			$V_{GS}=0V, I_{SD}=6.6A, T_j=175^{\circ}C$
Continuous Diode Forward Current	I_S		35		A	$V_{GS}=-4V, T_c=25^{\circ}C$
Body Diode Reverse Recovery Time	t_{rr}	-	17.6	-	ns	$V_R = 400V, I_D = 13.2A$
Body Diode Reverse Recovery Charge	Q_{rr}	-	82	-	nC	$di/dt = 1000A/\mu s$

Typical Performance Characteristics

Fig 1. Output Characteristic ($T_J = -55^\circ\text{C}$)

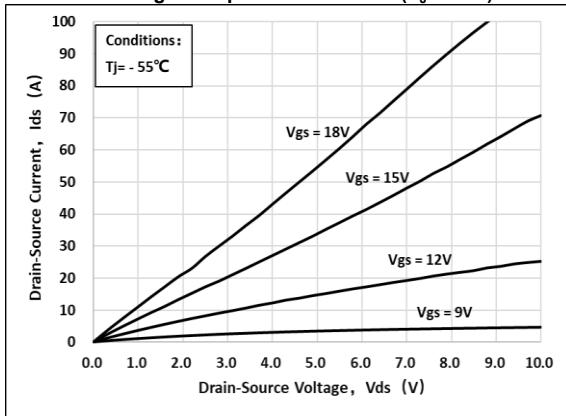


Fig 2. Output Characteristic ($T_J = 25^\circ\text{C}$)

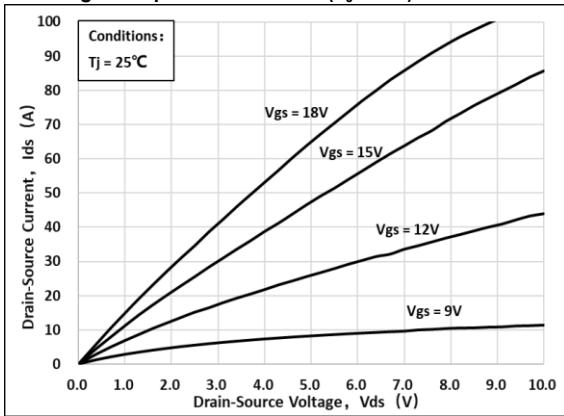


Fig 3. Output Characteristic ($T_J = 175^\circ\text{C}$)

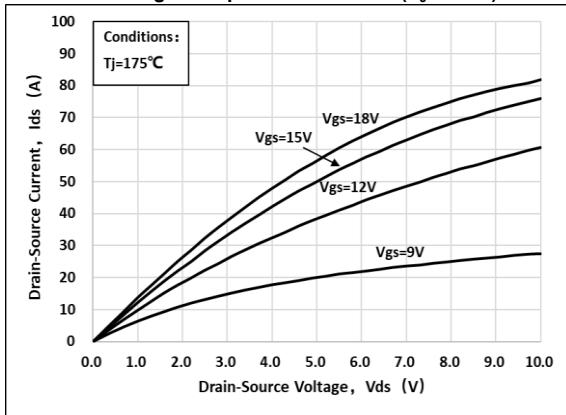


Fig 4: $R_{ds(on)}$ Vs Id_s Characteristic

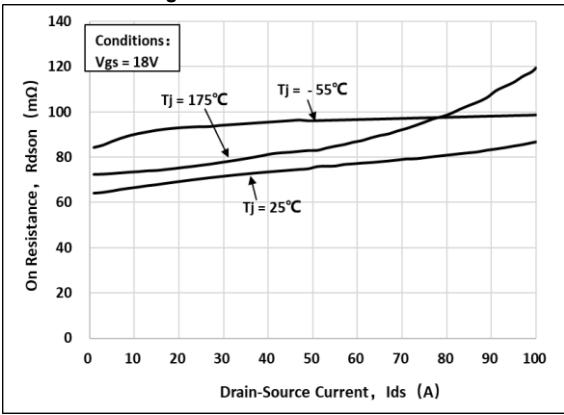


Fig 5: $R_{ds(on)}$ vs. Temperature

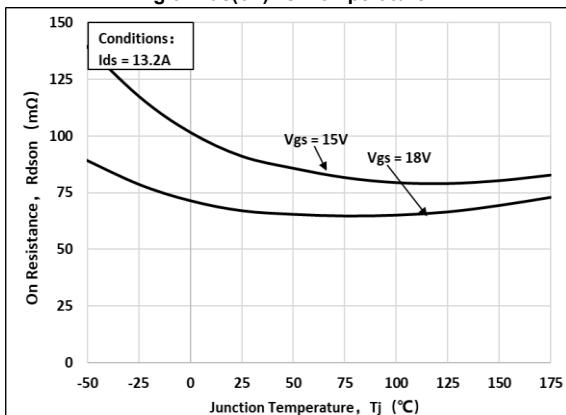


Fig 6: Transfer Characteristic

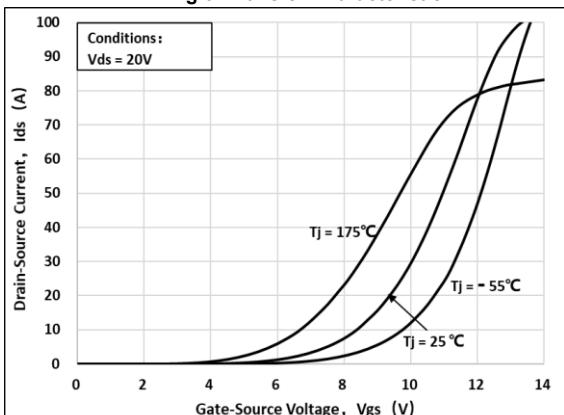


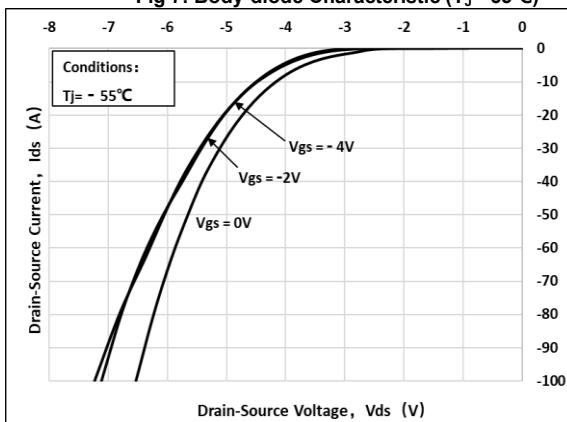
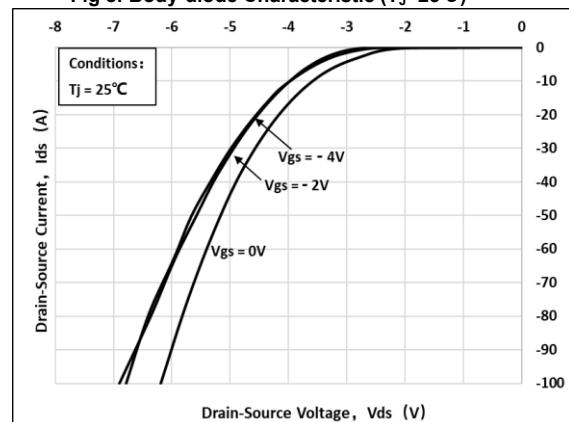
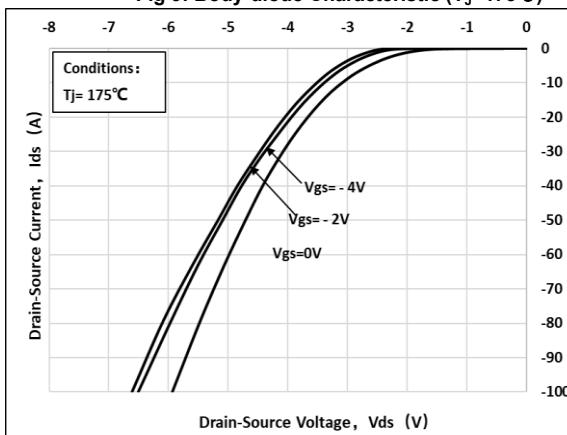
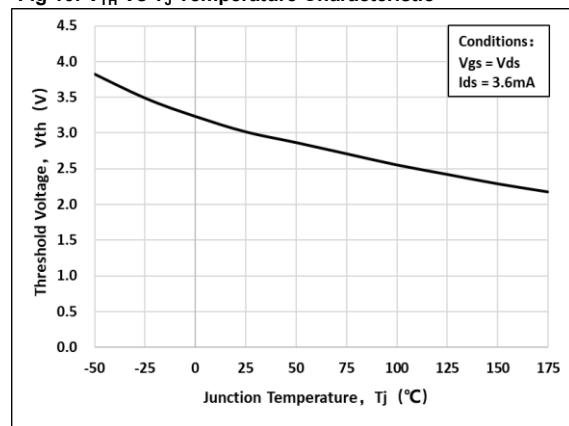
Fig 7: Body-diode Characteristic ($T_J = -55^\circ\text{C}$)Fig 8: Body-diode Characteristic ($T_J = 25^\circ\text{C}$)Fig 9: Body-diode Characteristic ($T_J = 175^\circ\text{C}$)Fig 10: V_{TH} Vs T_J Temperature Characteristic

Fig 11: Gate Charge Characteristics

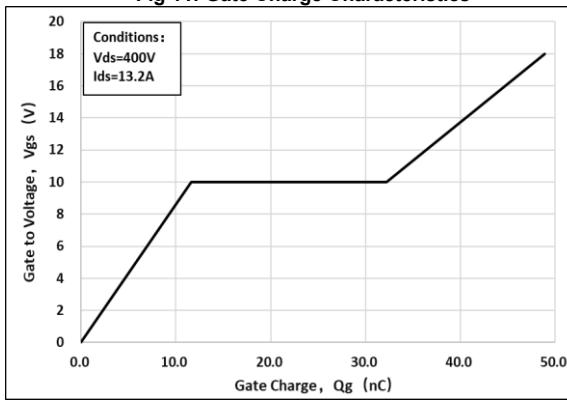
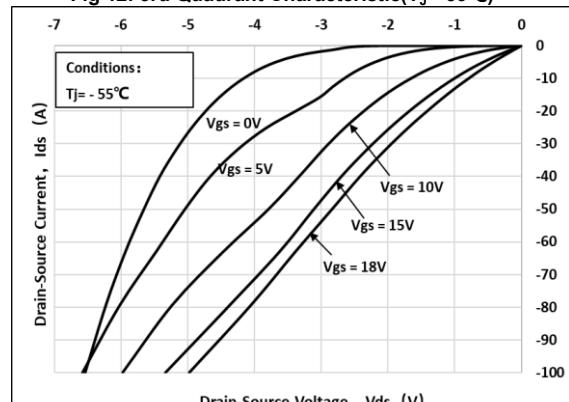
Fig 12: 3rd Quadrant Characteristic ($T_J = -55^\circ\text{C}$)

Fig 13: 3rd Quadrant Characteristic($T_J=25^\circ\text{C}$)

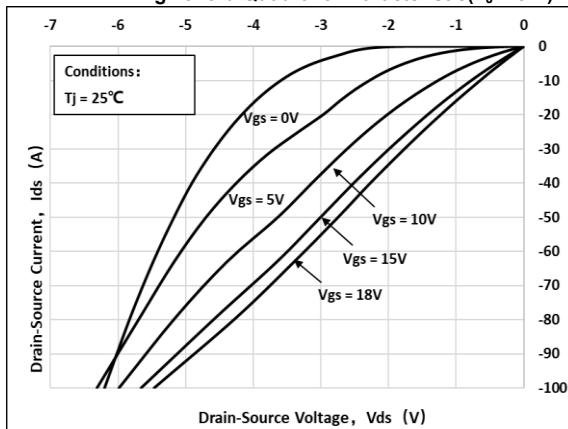


Fig 15: Capacitance Characteristic

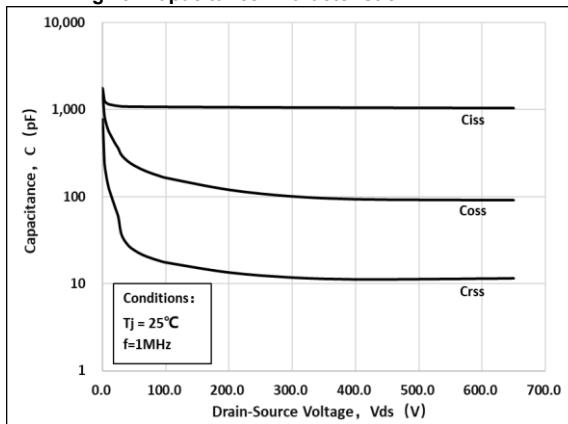


Fig 17: Transient Thermal Impedance

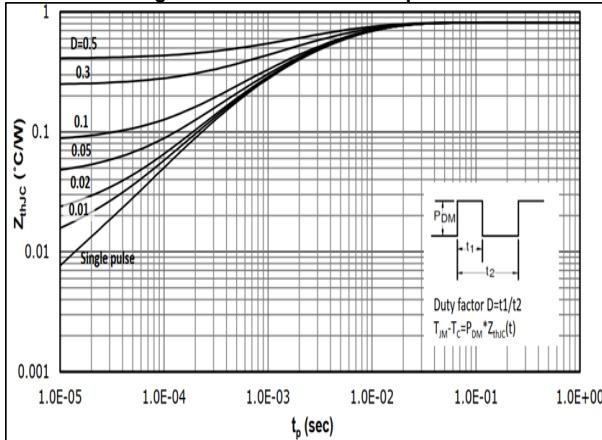


Fig 14: 3rd Quadrant Characteristic($T_J=175^\circ\text{C}$)

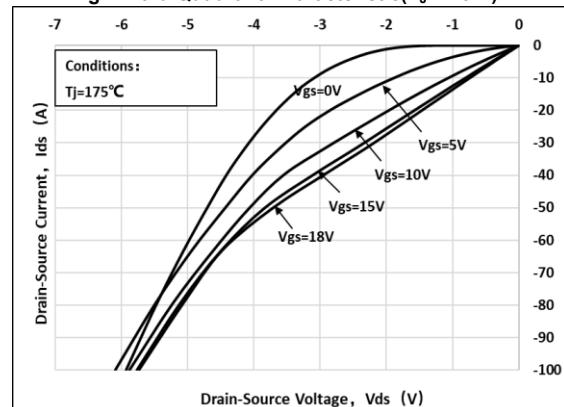
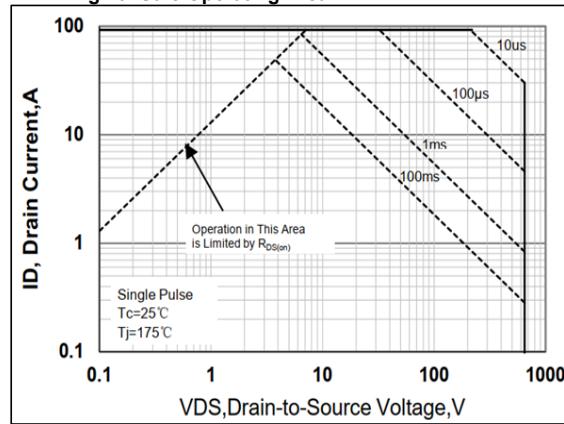


Fig 16: Safe Operating Area



Test Circuit & Waveform

Figure A. Definition of switching times

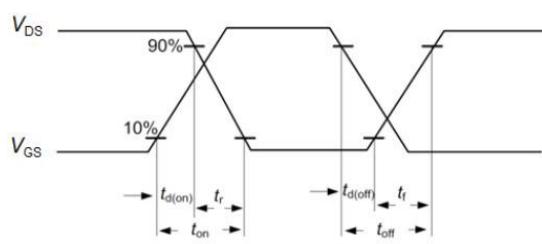


Figure B. Dynamic test circuit

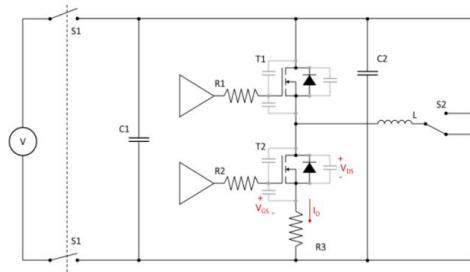
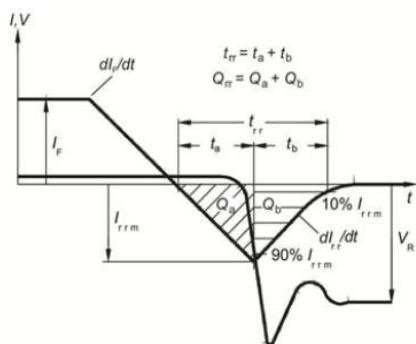
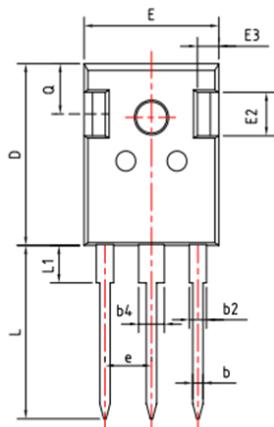


Figure C. Definition of body diodeswitching characteristics

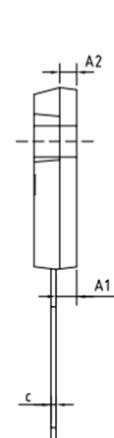


Package Outline:

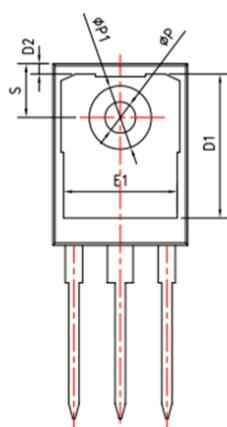
Top View



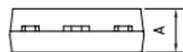
Side View



Bottom View



Front View



Dimension unit:mm			
SYMBOL	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.21	2.41	2.61
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.60	0.75
D	20.70	21.00	21.30
D1	16.25	16.55	16.85
D2	1.00	1.20	1.35
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44 BSC		
L	19.62	19.92	20.22
L1	-	-	4.30
φP	3.40	3.60	3.80
φP1	-	-	7.30
Q	5.40	5.80	6.20
S	6.20 BSC		

Contact Information

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For additional information, please contact your local Sales Representative.



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Product Specification Statement

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The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. TANI shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and TANI assumes no responsibility for the application of the product. TANI strives to provide accurate and up-to-date information to the best of our ability. However, due to technical, human, or other reasons, TANI cannot guarantee that the information provided in the product specification is entirely accurate and error-free. TANI shall not be held responsible for any losses or damages resulting from the use or reliance on any information in these product specifications.

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Users are advised to pay attention to the parameter limit values specified in the product specification and maintain a certain margin in design or application to ensure that the product does not exceed the parameter limit values defined in the product specification. This precaution should be taken to avoid exceeding one or more of the limit values, which may result in permanent irreversible damage to the product, ultimately affecting the quality and reliability of the system or equipment.

The design of the product is intended to meet civilian needs and is not guaranteed for use in harsh environments or precision equipment. It is not recommended for use in systems or equipment such as medical devices, aircraft, nuclear power, and similar systems, where failures in these systems or equipment could reasonably be expected to result in personal injury. TANI shall assume no responsibility for any consequences resulting from such usage.

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