T1M45065K1

SiC N-MOSFET 650V, 45mΩ, 49A

Features

- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Avalanche Ruggednes

Product Summary

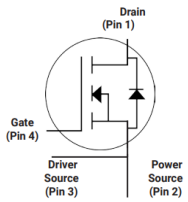
V_{DS}	650V
$R_{DS(on)\_typ}$	45mΩ
I_D	49A

Applications

- Solar Inverters
- Switch Mode Power Supplies
- High Voltage DC-DC Converters
- Batterry Chargers

RoHS

Halogen Free



Package Marking and Ordering Information

Part #	Marking	Package
T1M45065K1	1M45065K1	TO-247-4

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Continuous drain current V_{GS}=18V T_C = 25°C V_{GS}=18V T_C = 100°C	I_D	 49 35	A
Pulsed drain current (T_C = 25°C, t_p limited by T_{jmax})	$I_{D\ pulse}$	123	A
Avalanche energy, single pulse (L=10mH)	E_{AS}	1000	mJ
Gate-Source voltage	V_{GSOP}	-4/+18	V
Gate-Source voltage（dynamic,Absolute maximum values）	V_{GSmax}	-8/+22	V
Power dissipation (T_C = 25°C)	P_{tot}	241	W
Operating junction and storage temperature	T_j , T_{stg}	-55...+175	°C

•Example of acceptable V_{GS} waveform

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	R_{thJC}	0.62	°C/W
Thermal resistance, junction – ambient. Max	R_{thJA}	40	

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Static Characteristic						
Drain-source breakdown voltage	BV_{DSS}	650	-	-	V	V_{GS}=0V, I_D=250uA
Gate threshold voltage	$V_{GS(th)}$	2	-	4	V	V_{DS}=V_{GS},I_D=4.8mA
Zero gate voltage drain current	I_{DSS}	-	1	100	μA	V_{DS}=650V,V_{GS}=0V T_j=25°C
		-	10	-		T_j=175°C
Gate-source leakage current	I_{GSS}	-		250	nA	V_{GS}=18V,V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	-	45	59	mΩ	V_{GS}=18V, I_D=17.6A, T_j=25°C
		-	55	-		T_j=175°C
Transconductance	g_{fs}	-	6.4	-	S	V_{DS}=20V,I_D=17.6A
Dynamic Characteristic						
Input Capacitance	C_{iss}	-	1509	-	pF	V_{DS} = 650V V_{GS} = 0V T_J = 25°C V_{AC} = 25mV f = 1MHz
Output Capacitance	C_{oss}	-	130	-		
Reverse Transfer Capacitance	C_{rss}	-	16	-		
Gate Total Charge	Q_G	-	69.9	-	nC	V_{DS} = 400V V_{GS} = 0/18V I_D = 17.6A
Gate-Source charge	Q_{gs}	-	15.4	-		
Gate-Drain charge	Q_{gd}	-	28	-		
Turn-On Switching Energy	E_{ON}	-	87.4	-	μJ	V_{DD} = 400V V_{GS} = -4/+18V I_D = 17.6A R_G = 5Ω L = 100uH
Turn-Off Switching Energy	E_{OFF}	-	24	-		
Turn-on delay time	$t_{d(on)}$	-	10.56	-	ns	
Rise time	t_r	-	4.16	-		
Turn-off delay time	$t_{d(off)}$	-	19.52	-		
Fall time	t_f	-	6.4	-		
Gate resistance	R_G	-	0.9	-	Ω	V_{AC} = 25mV, f=1MHz

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}		3.2		V	V_{GS}=0V,I_{SD}=8.8A, T_J=25°C
			2.7			V_{GS}=0V,I_{SD}=8.8A, T_J=175°C
Continuous Diode Forward Current	I_S		48		A	V_{GS}=-4V, T_C=25°C
Body Diode Reverse Recovery Time	t_{rr}	-	20.4	-	ns	V_R = 400V, I_D = 17.6A di/dt = 1000A/μS
Body Diode Reverse Recovery Charge	Q_{rr}	-	114.1	-	nC	

Typical Performance Characteristics

Fig 1. Output Characteristic (T_J=-55℃)

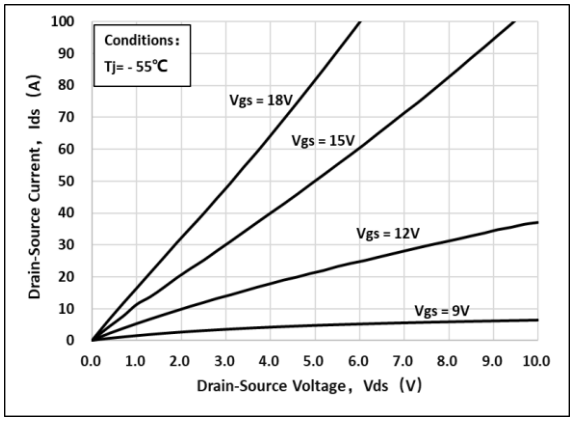


Fig 2. Output Characteristic (T_J=25℃)

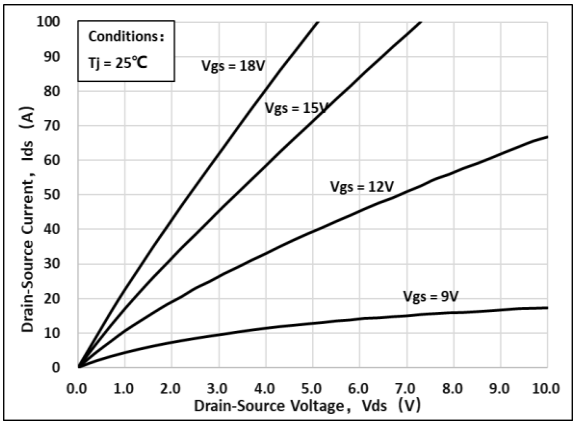


Fig 3. Output Characteristic (T_J=175℃)

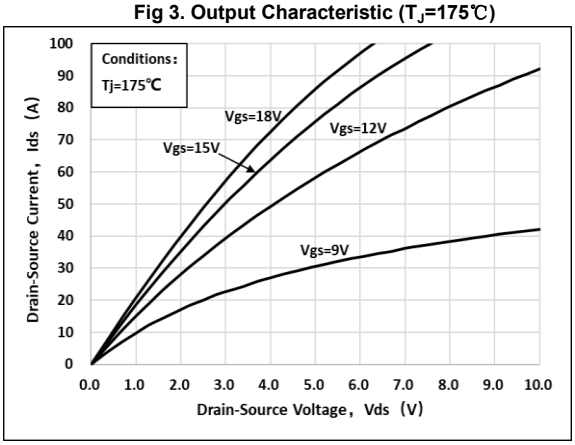


Fig 4: Rdson Vs Ids Characteristic

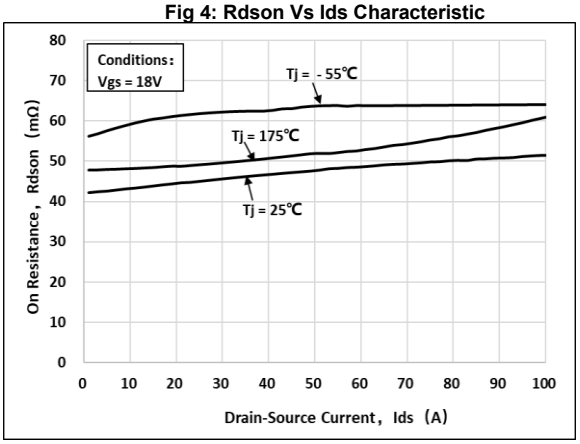


Fig 5: Rds(on) vs. Temperature

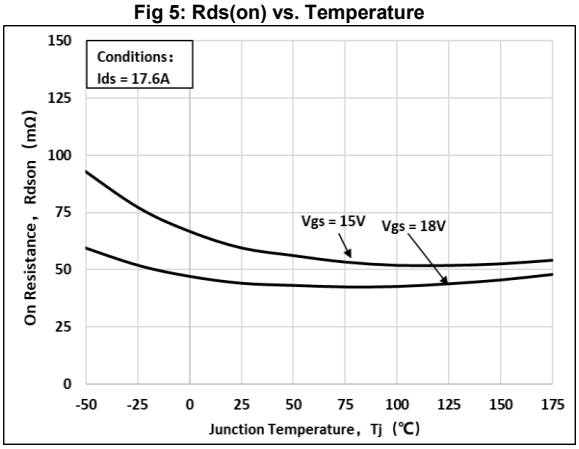


Fig 6: Transfer Characteristic

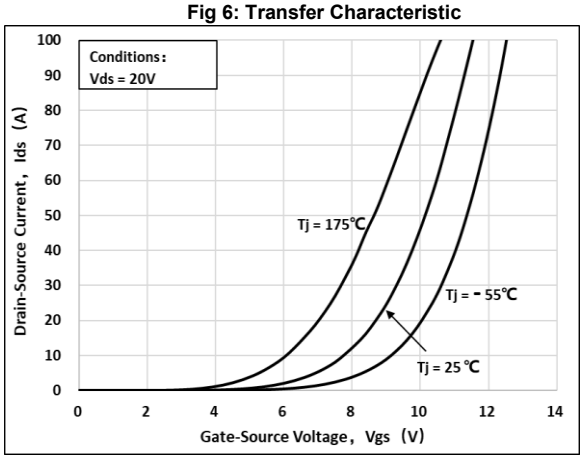


Fig 7: Body-diode Characteristic (T_J=-55°C)

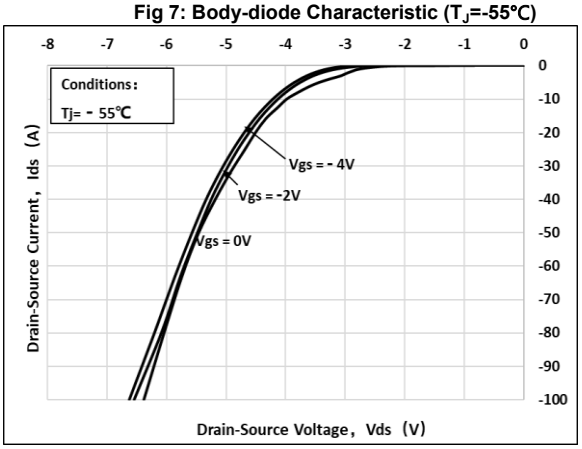


Fig 8: Body-diode Characteristic (T_J=25℃)

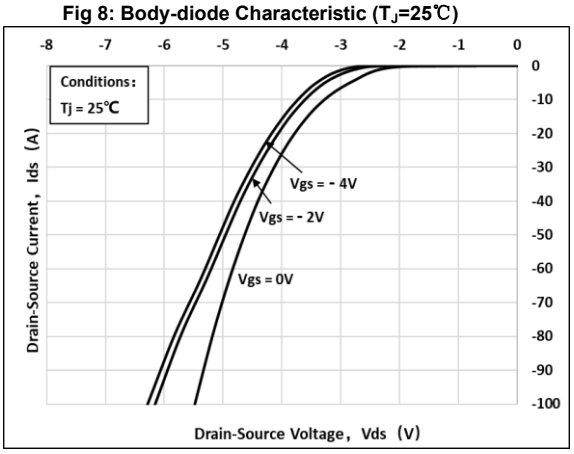


Fig 9: Body-diode Characteristic (T_J=175℃)

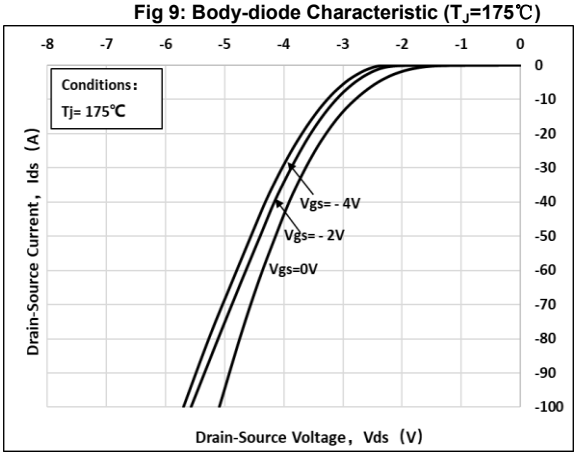


Fig 10: V_{TH} Vs T_J Temperature Characteristic

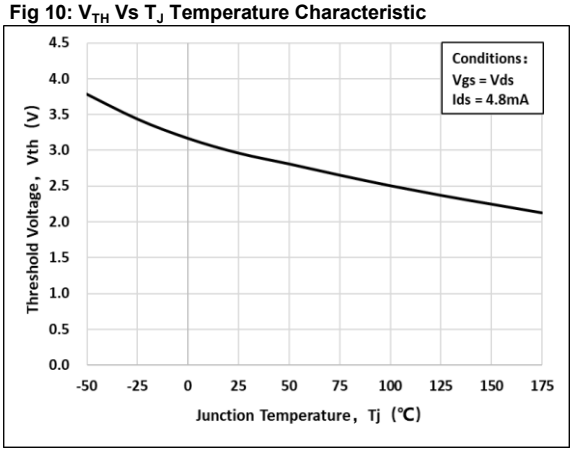


Fig 11: Gate Charge Characteristics

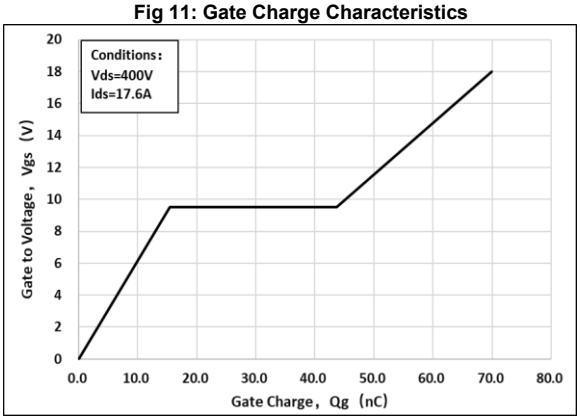


Fig 12: 3rd Quadrant Characteristic(T_J=-55°C)

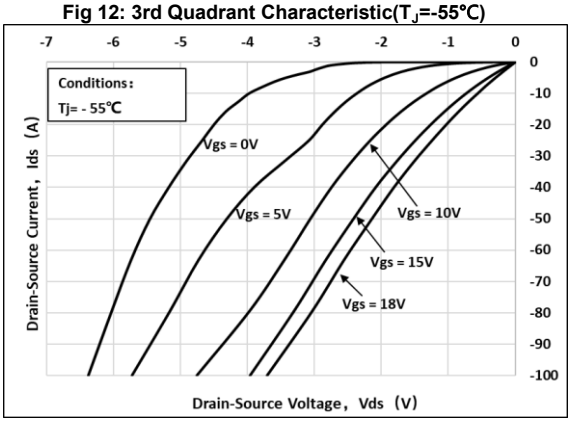


Fig 13: 3rd Quadrant Characteristic(T_J=25℃)

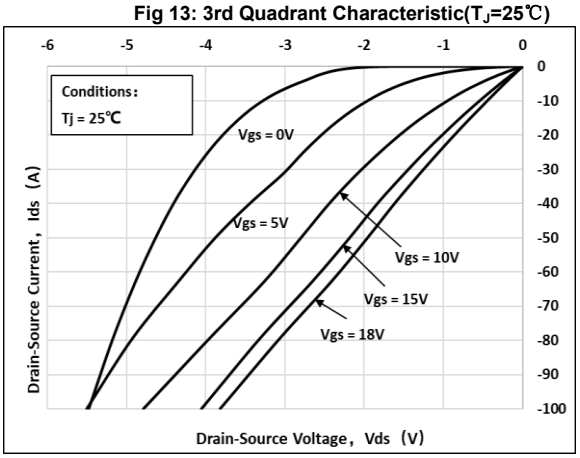


Fig 14: 3rd Quadrant Characteristic(T_J=175℃)

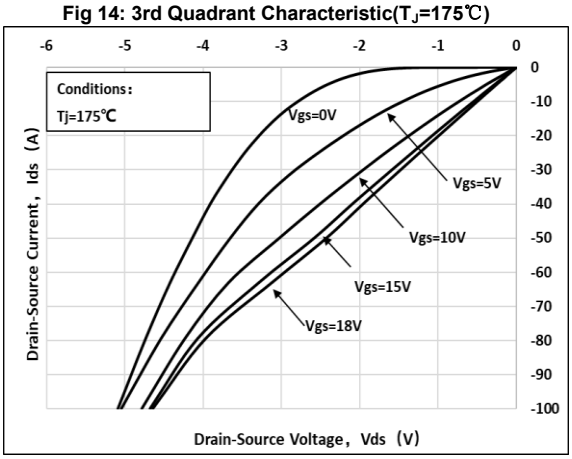


Fig 15: Capacitance Characteristic

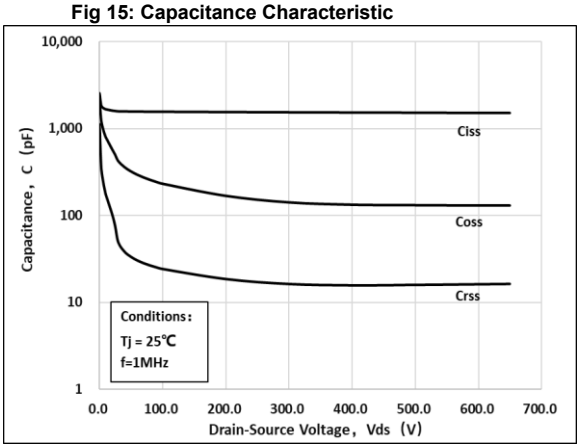


Fig 16: Safe Operating Area

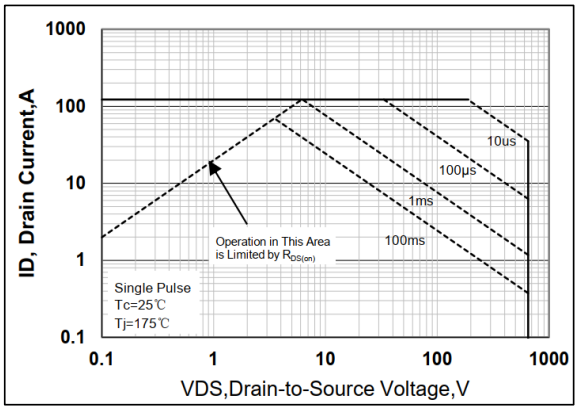
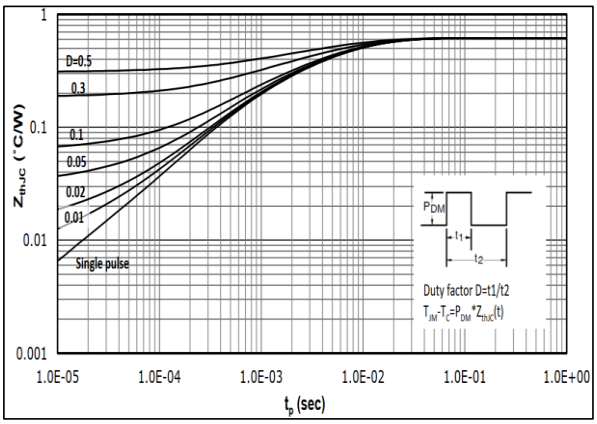


Fig 17: Transient Thermal Impedance



Test Circuit & Waveform

Figure A. Definition of switching times

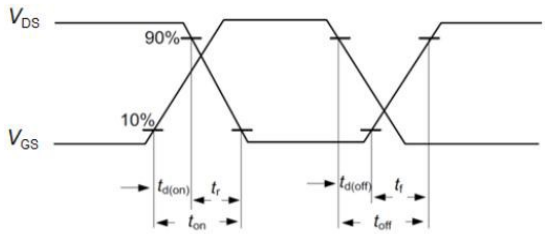


Figure B. Dynamic test circuit

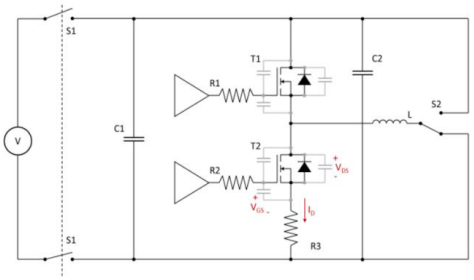
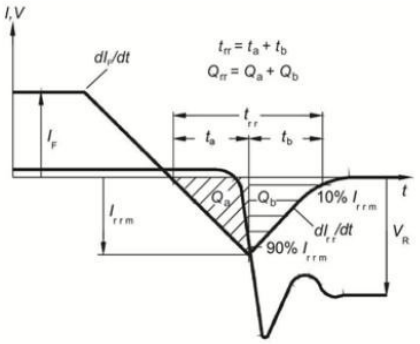
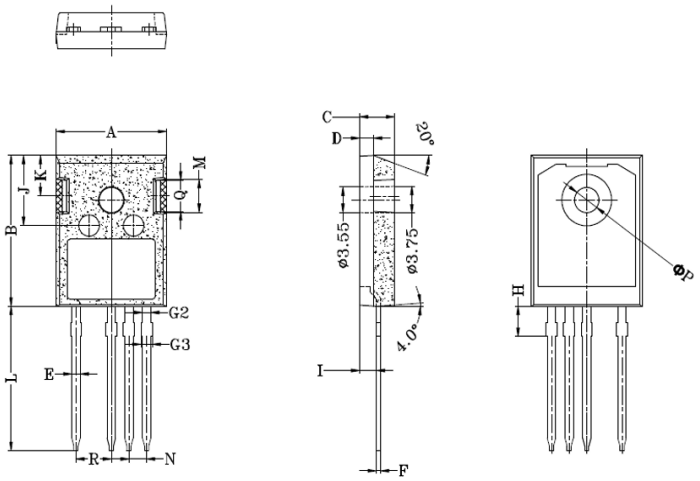


Figure C. Definition of body diodeswitching characteristics



Package Outline:



项目	规范(mm)	
	MIN	MAX
A	15.80	16.00
B	20.90	21.10
C	4.90	5.10
D	1.90	2.10
E	1.10	1.30
F	0.50	0.70
G2	1.10	1.30
G3	1.18	1.38
H	4.18	4.38
I	2.30	2.50
J	9.65	9.85
K	5.54	5.74
L	19.80	20.20
M	4.50	4.70
N	2.34	2.74
ΦP	3.40	3.60
Q	4.232	4.432
R	4.88	5.28

Contact Information

TANI website: **http://www.tanisemi.com** Email:tani@tanisemi.com

For additional information, please contact your local Sales Representative.

Product Specification Statement

The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.

The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. TANI shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and TANI assumes no responsibility for the application of the product. TANI strives to provide accurate and up -to-date information to the best of our ability. However, due to technical, human, or other reasons, TANI cannot guarantee that the information provided in the product specification is entirely accurate and error-free. TANI shall not be held responsible for any losses or damages resulting from the use or reliance on any information in these product specifications.

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Users are advised to pay attention to the parameter limit values specified in the product specification and maintain a certain margin in design or application to ensure that the product does not exceed the parameter limit values defined in the product specification. This precaution should be taken to avoid exceeding one or more of the limit values, which may result in permanent irreversible damage to the product, ultimately affecting the quality and reliability of the system or equipment.

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