

TN9928ANPA

Dual N-Channel Enhancement Mode Power MOSFET

Product Summary

- V_{DS}= 20V,I_D= 10A
- $R_{DS(on)} < 20 m\Omega @V_{GS} = 4.5V$
- $R_{DS(on)} < 28m\Omega @V_{GS} = 2.5V$

Features

- Advanced Trench Technology
- RoHS and Reach Compliant
- Halogen and Antimony Free
- Moisture Sensitivity Level 3

Application

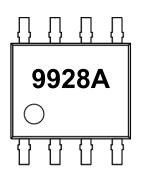
- Power Switching Application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply

SOP-8

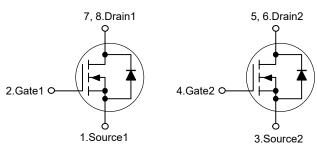
(Top View)

Pin	Description	Pin	Description
1	Source1	4	Gate2
2	Gate1	5,6	Drain2
3	Source2	7,8	Drain1

Marking Code



Schematic Diagram



N-Channel

N-Channel

Absolute Maximum Ratings

Ratings at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±12	V
Drain Current-Continuous	I _D	10	Α
Drain Current-Pulsed Note1	I _{DM}	40	Α
Maximum Power Dissipation	P _D	2.0	W
Junction Temperature	TJ	150	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

Thermal Characteristics

Thermal Resistance,Junction-to-Ambient Note2	R _{θJA}	62.5	°C/W
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Electrical Characteristics

(T_J=25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Sourtce Breakdown Voltage	V _{GS} =0V,I _D =250 μ A	20			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =20V			1	μА
I _{GSS}	Gate-Source Leakage Current	V_{GS} = \pm 12V, V_{DS} =0A			±100	nA
On Characteristics						
$V_{GS(th)}$	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	0.5	0.7	1	V
	Drain-Source On Resistance②	V _{GS} =4.5V,I _D =4A		16	20	mΩ
R _{DS(ON)}		V _{GS} =2.5V,I _D =3A		20	28	
Dynamic Characteris	etics					
C _{iss}	Input Capacitance			775		
C _{oss}	Output Capacitance	V _{DS} =10V, V _{GS} =0V, f=1MHz		135		pF
C _{rss}	Reverse Transfer Capacitance			75		
Switching Character	istics				•	
t _{d(on)}	Turn-On Delay Time			8.5		ns
t _r	Rise Time	V _{DD} =10V, I _D =3.5A		29		ns
t _{d(off)}	Turn-Off Delay Time	$R_G=3~\Omega$. $V_{GS}=4.5$ V,		34		ns
t f	Fall Time			9.5		ns
\mathbf{Q}_{g}	Total Gate Charge			10		nC
\mathbf{Q}_{gs}	V _{GS} =4.5V, V _{DS} =10V,			2.2		nC
\mathbf{Q}_{gd}	Gate-Drain "Miller" Charge	I _D =3.5A		2.8		nC
Drain-Source Diode	Characteristics		•			
V_{SD}	Forward Voltage②	V _{GS} =0V,I _{SD} =6.8A			1.2	V
Is	Source drain current(Body Diode)	T _A =25℃			6.8	А

Notes:

- 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
- 2. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%

Typical Characteristic Curves

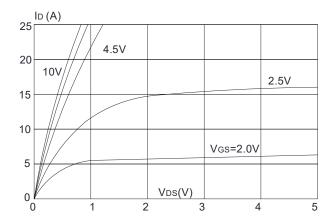


Figure1: Output Characteristics

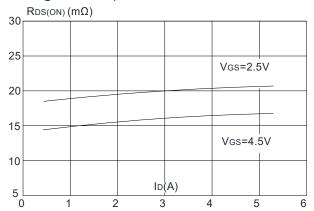


Figure 3:On-resistance vs. Drain Current

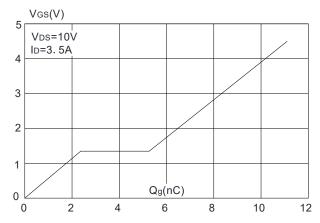


Figure 5: Gate Charge Characteristics

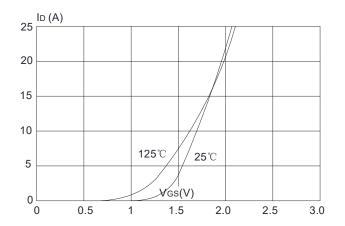


Figure 2: Typical Transfer Characteristics

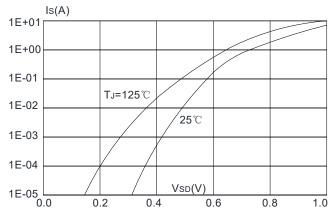


Figure 4: Body Diode Characteristics

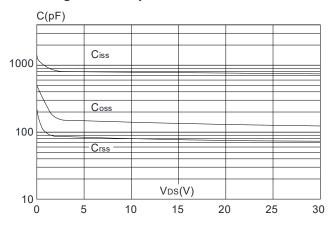


Figure 6: Capacitance Characteristics

Typical Characteristic Curves

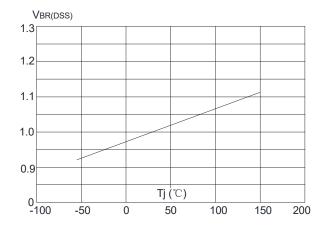


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

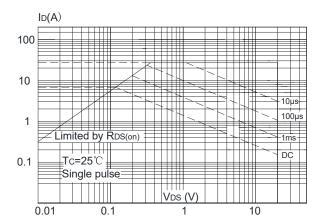


Figure 9: Maximum Safe Operating Area

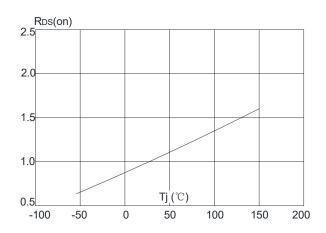


Figure 8: Normalized on Resistance vs. Junction Temperature

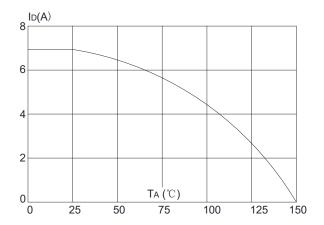


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

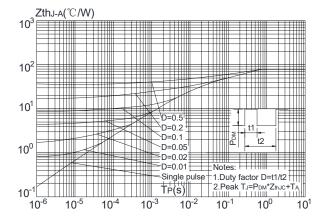
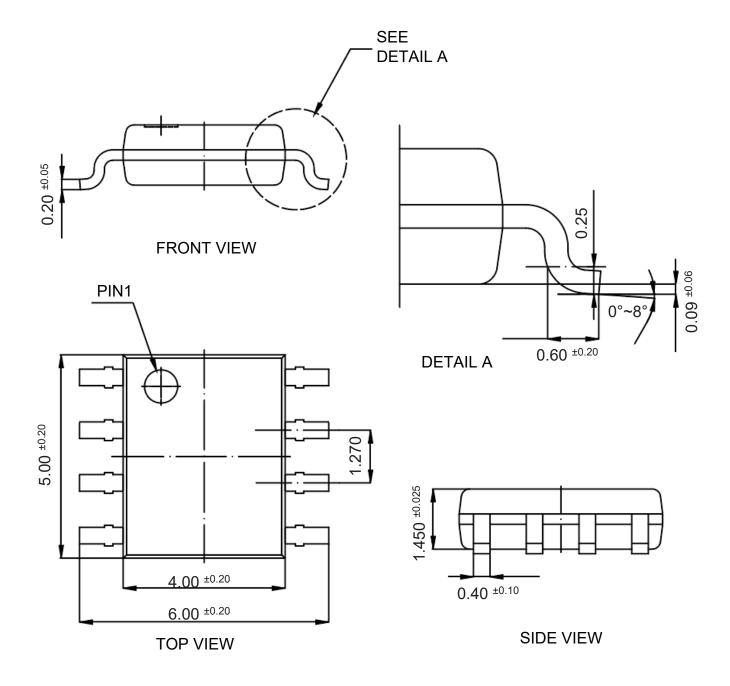


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

Package Outline

SOP-8 Dimensions in mm



Ordering Information

Device	Package	Shipping
TN9928ANPA	SOP-8	4,000PCS/Reel&13inches

Contact Information

TANI website: http://www.tanisemi.com Email:tani@tanisemi.com

For additional information, please contact your local Sales Representative.



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