

Product Summary

- $V_{DS} = 40V, I_D = 30A$
- $R_{DS(on)} < 17m\Omega @ V_{GS} = 10V$
- $R_{DS(on)} < 25m\Omega @ V_{GS} = 4.5V$

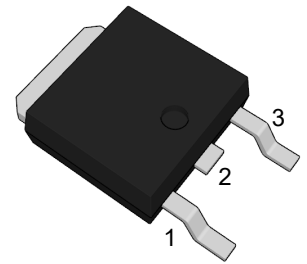
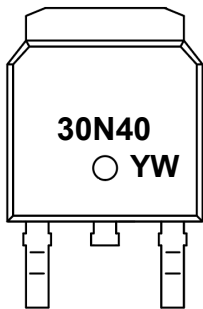
Features

- Advanced Trench Technology
- 100% Avalanche Tested
- RoHS Compliant
- Halogen and Antimony Free
- Moisture Sensitivity Level 3

Application

- Load Switch
- PWM Application
- Power management

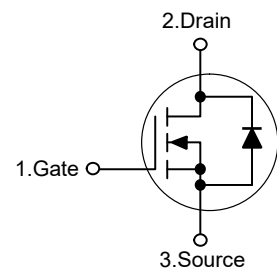
Marking Code



(Top View)

Pin	Description
1	Gate
2	Drain
3	Source

Schematic Diagram



Absolute Maximum Ratings

Ratings at 25°C case temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	30	A
Drain Current-Pulsed ^{Note1}	I_{DM}	80	A
Single Pulse Avalanche Energy ^{Note2}	E_{AS}	31.3	mJ
Maximum Power Dissipation	P_D	31.3	W
Junction Temperature	T_J	150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

Thermal Characteristics

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	4	°C/W
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Electrical Characteristics

(T_J=25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
V _{(BR)DSS}	Drain-Sourtce Breakdown Voltage	V _{GS} =0V,I _D =250 μ A	40	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =32V,T _J = 25 °C	---	---	1	μ A
		V _{GS} =0V, V _{DS} =32V,T _J = 55 °C	---	---	5	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	Gate-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1	---	2.5	V
R _{DS(ON)}	Drain-Source On Resistance ³	V _{GS} =10V,I _D =15A	---	16	20	m Ω
		V _{GS} =4.5V,I _D =10A	---	23	29	m Ω
G _{FS}	Forward Transconductance	V _{DS} =5V, I _D =15A	---	34	---	S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	2.1	---	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	1010	---	pF
C _{oss}	Output Capacitance		---	102	---	
C _{rss}	Reverse Transfer Capacitance		---	70	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} =20V,I _D =15A, R _G =3.3Ω, V _{GS} =10V	---	2.8	---	ns
t _r	Rise Time		---	12.8	---	ns
t _{d(off)}	Turn-Off Delay Time	V _{DD} =20V,I _D =15A, R _G =3.3Ω, V _{GS} =10V	---	21.2	---	ns
t _f	Fall Time		---	6.4	---	ns
Q _g	Total Gate Charge	V _{DS} =32V, V _{GS} =4.5V, I _D =15A	---	10	---	nC
Q _{gs}	Gate-Source Charge		---	2.55	---	nC
Q _{gd}	Gate-Drain Charge		---	4.8	---	nC

Drain-Source Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage	$I_S=1A, V_{GS}=0V, T_J = 25\text{ }^{\circ}C$	---	---	1.2	V
I_S	Continuous Source Current	$V_G=V_D=0V$	---	---	30	A
I_{SM}	Pulsed Source Current		---	---	80	A
T_{rr}	Reverse Recovery Time	$I_F= 15\text{ A}, dI_F / dt = 100\text{ A} / \mu s$	---	10	---	ns
Q_{rr}	Reverse Recovery Charge	$T_J = 25\text{ }^{\circ}C$	---	3.1	---	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition : $T_J=25\text{ }^{\circ}C, V_{DD}=20V, V_G=10V, L=0.5mH$
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Characteristics: ($T_J=25\text{ }^{\circ}C$ unless otherwise noted)

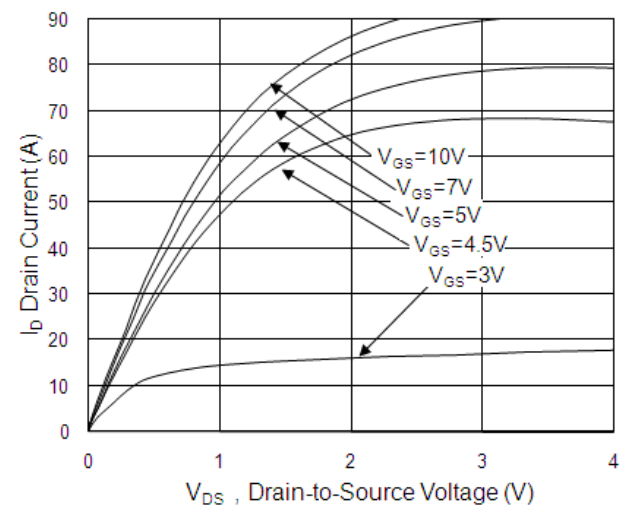


Fig.1 Typical Output Characteristics

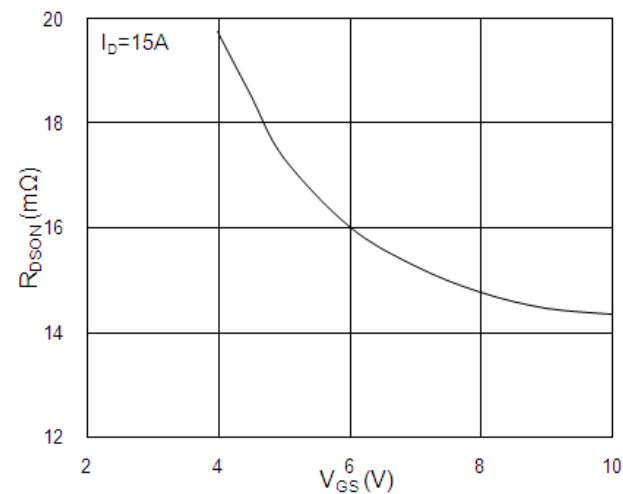


Fig.2 On-Resistance vs. G-S Voltage

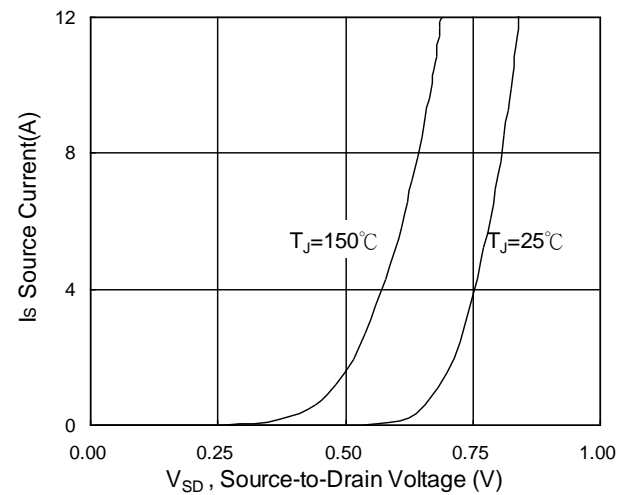


Fig.3 Forward Characteristics Of Reverse

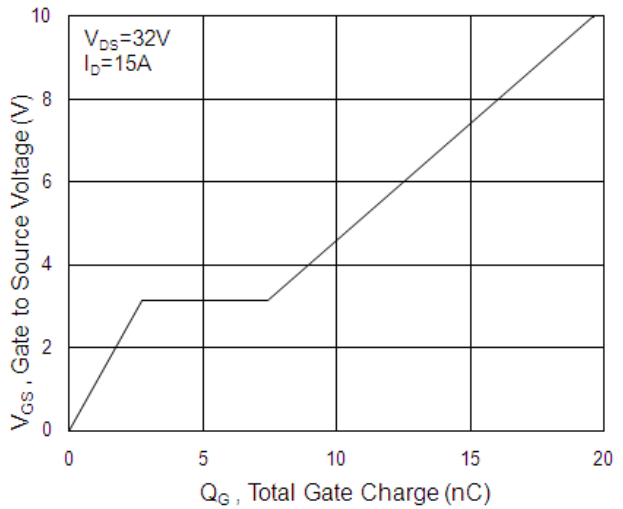


Fig.4 Gate-Charge Characteristics

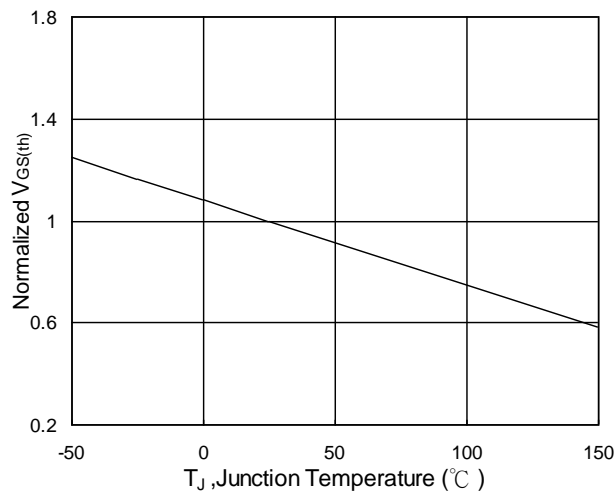


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

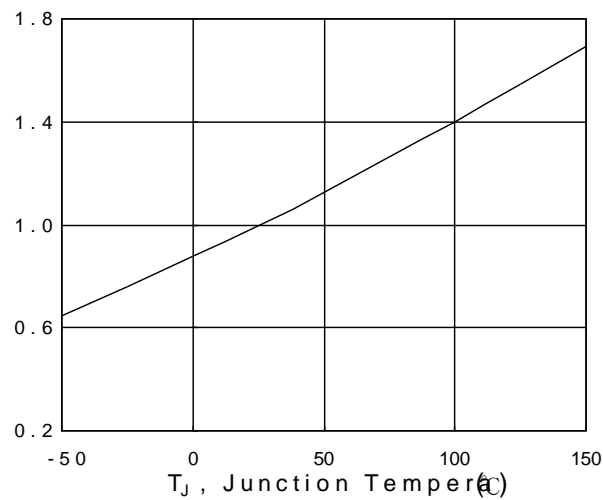


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

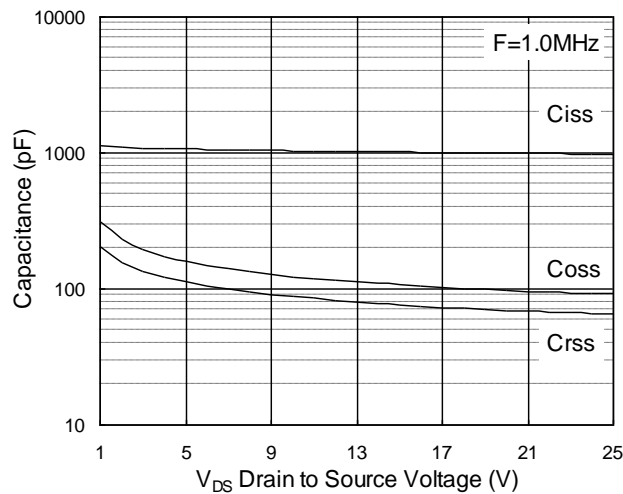


Fig.7 Capacitance

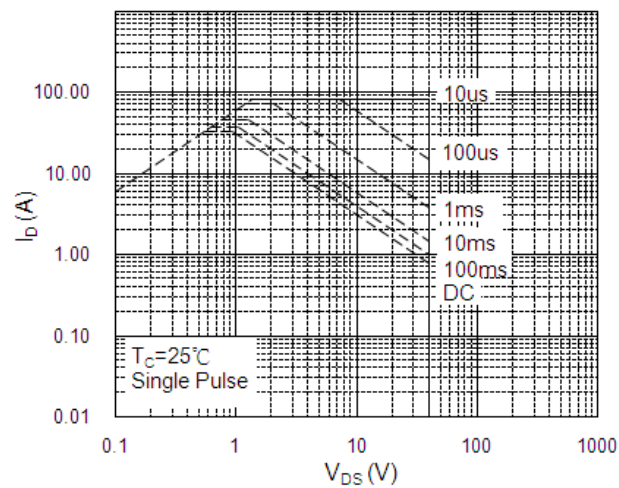


Fig.8 Safe Operating Area

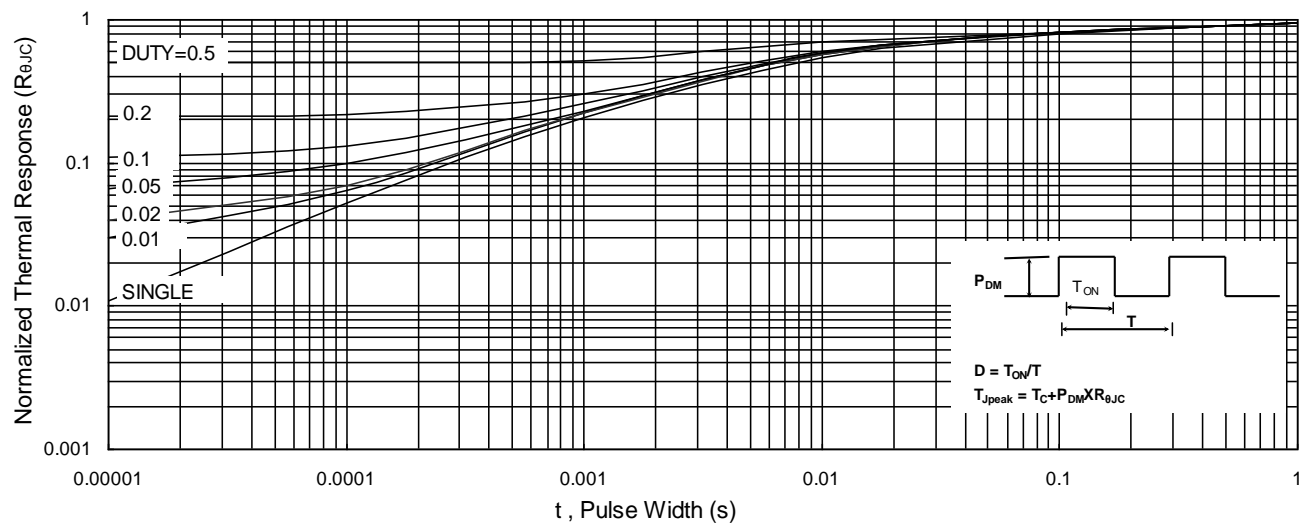
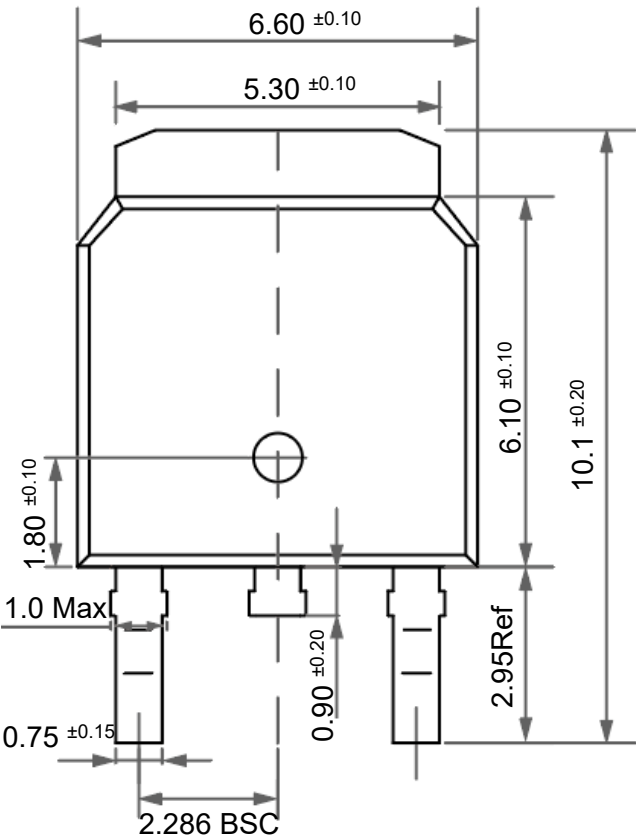


Fig.9 Normalized Maximum Transient Thermal Impedance

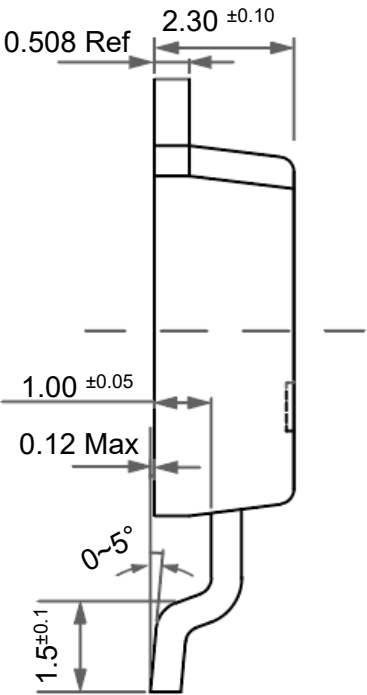
Package Outline

TO-252

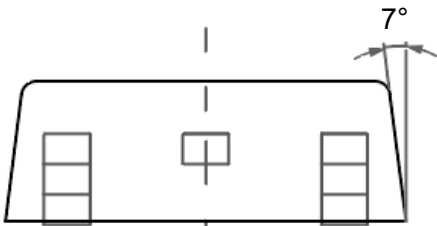
Dimensions in mm



Front View



Side View



Bottom View

Ordering Information

Device	Package	Shipping
TN30N40TE	TO-252	2,500PCS/Reel&13inches

Contact Information

TANI website: <http://www.tanisemi.com> Email: tani@tanisemi.com

For additional information, please contact your local Sales Representative.



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