

### Product Summary

- $V_{DS} = -20V, I_D = -80A$
- $R_{DS(on)} < 4.5m\Omega @ V_{GS} = -4.5V$
- $R_{DS(on)} < 6m\Omega @ V_{GS} = -2.5V$

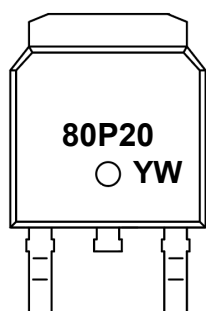
### Features

- Advanced Trench Technology
- 100% Avalanche Tested
- RoHS Compliant
- Halogen and Antimony Free
- Moisture Sensitivity Level 3

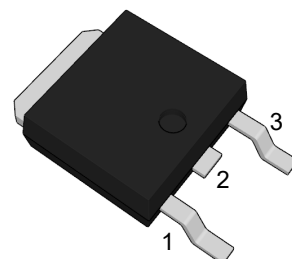
### Application

- Load Switch
- PWM Application
- Power management

### Marking Code



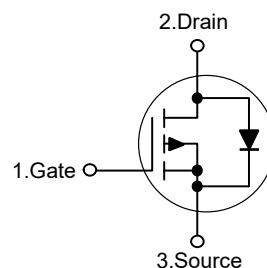
### TO-252



(Top View)

Pin	Description
1	Gate
2	Drain
3	Source

### Schematic Diagram



### Absolute Maximum Ratings

Ratings at 25°C case temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$-V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$-I_D$	80	A
Drain Current-Pulsed <sup>Note1</sup>	$-I_{DM}$	280	A
Maximum Power Dissipation	$P_D$	43	W
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-55 to +150	°C

### Thermal Characteristics

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.90	°C/W
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## Electrical Characteristics

(T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter		Test condition	Min.	Typ.	Max.	Units
Static Characteristics							
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage		V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-20	-	-	V
I <sub>GSS</sub>	Gate-body Leakage current		V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±12V	-	-	±100	nA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	T <sub>J</sub> =25°C	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V	-	-	-1	μA
		T <sub>J</sub> =100°C		-	-	-100	μA
V <sub>GS(th)</sub>	Gate-Threshold Voltage		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-0.4	-0.7	-1	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance <sup>4</sup>		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -10A	-	3.8	4.5	mΩ
			V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -10A	-	5	6.0	mΩ
g <sub>fs</sub>	Forward Transconductance <sup>4</sup>		V <sub>DS</sub> = -4.5V, I <sub>D</sub> = -10A	-	56	-	S
Dynamic Characteristics <sup>5</sup>							
C <sub>iss</sub>	Input Capacitance		V <sub>DS</sub> = -10V, V <sub>GS</sub> =0V, f=1MHz	-	4770	-	pF
C <sub>oss</sub>	Output Capacitance			-	665	-	
C <sub>rss</sub>	Reverse Transfer Capacitance			-	570	-	
R <sub>g</sub>	Gate Resistance		f =1MHz	-	9.6	-	Ω
Switching Characteristics <sup>5</sup>							
Q <sub>g</sub>	Total Gate Charge		V <sub>GS</sub> = -4.5V,V <sub>DS</sub> = -10V, I <sub>D</sub> = -10A	-	55	-	nC
Q <sub>gs</sub>	Gate-Source Charge			-	5.2	-	
Q <sub>gd</sub>	Gate-Drain Charge			-	10	-	
t <sub>d(on)</sub>	Turn-On Delay Time		V <sub>GS</sub> = -4.5V, V <sub>DD</sub> = -10V, R <sub>G</sub> = 3Ω, I <sub>D</sub> = -10A	-	22	-	ns
t <sub>r</sub>	Rise Time			-	38	-	
t <sub>d(off)</sub>	Turn-Off Delay Time			-	110	-	
t <sub>f</sub>	Fall Time			-	62	-	
Drain-Source Body Diode Characteristics							
V <sub>SD</sub>	Diode Forward Voltage <sup>4</sup>		I <sub>S</sub> = -10A, V <sub>GS</sub> = 0V	-	-	-1.2	V
I <sub>S</sub>	Continuous Source Current	T <sub>C</sub> =25°C	-	-	-	-80	A

## Notes:

- 1.Repetitive rating, pulse width limited by junction temperature T<sub>J</sub>(MAX)=150°C.
- 2.The EAS data shows Max. rating. The test condition is V<sub>DD</sub>= -25V, V<sub>GS</sub>= -10V, L= 0.4mH, I<sub>AS</sub>= -20A.
- 3.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- 4.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.

This value is guaranteed by design hence it is not included in the production test

Typical Characteristic Curves

Figure1: Output Characteristics

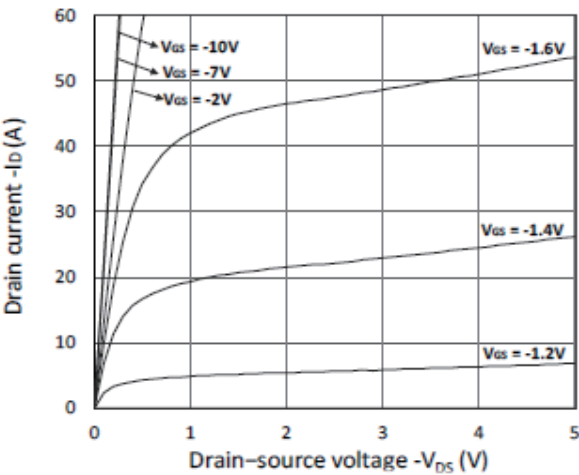


Figure 2: Typical Transfer Characteristics

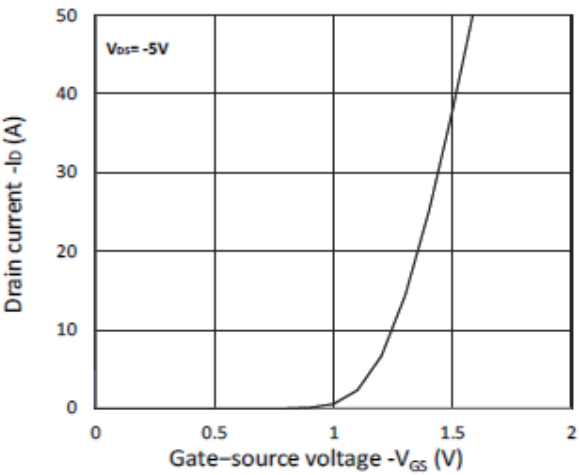


Figure 3:Forward Characteristics of Reverse

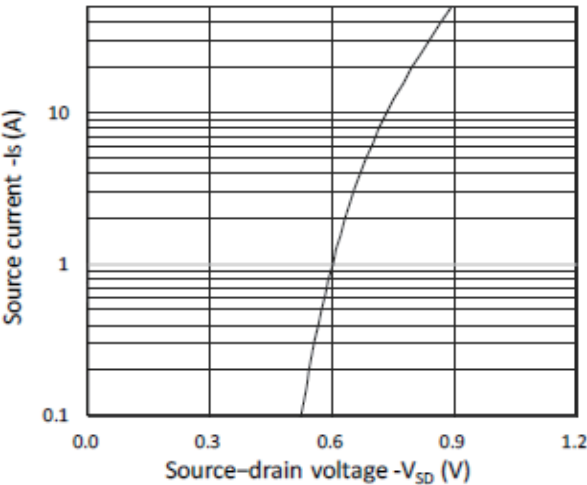


Figure 4: R\_DS(ON) vs. V\_GS

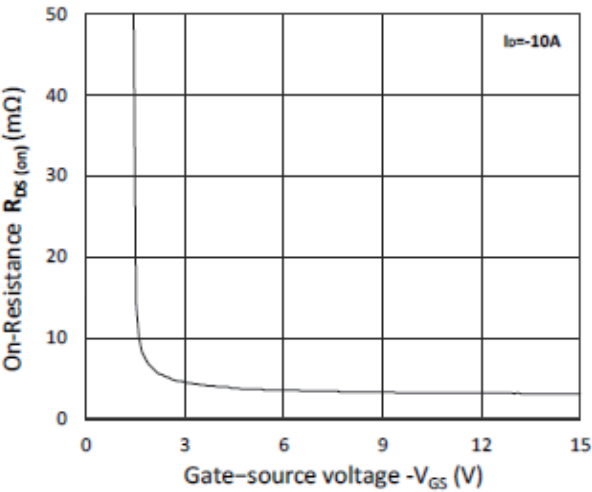


Figure 5: R\_DS(ON) vs. I\_D

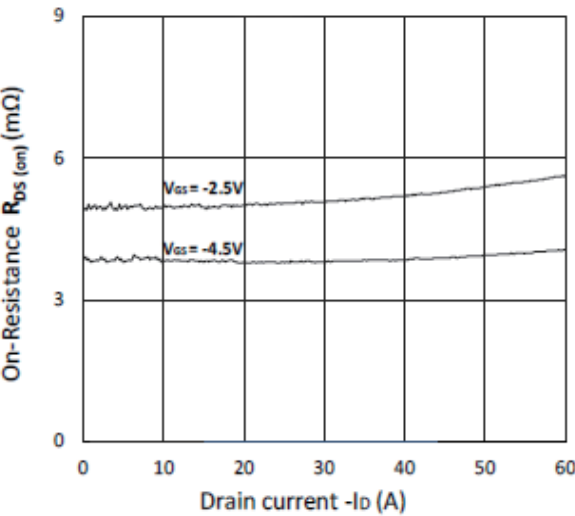


Figure 6:Normalized R\_DS(on) vs. Temperature

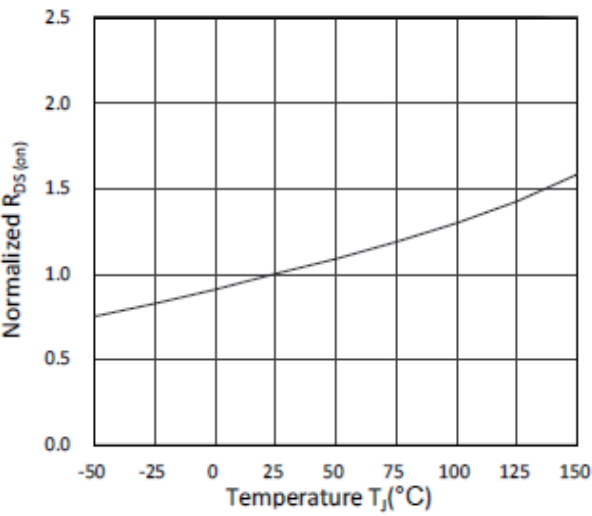


Figure 7: Capacitance Characteristics

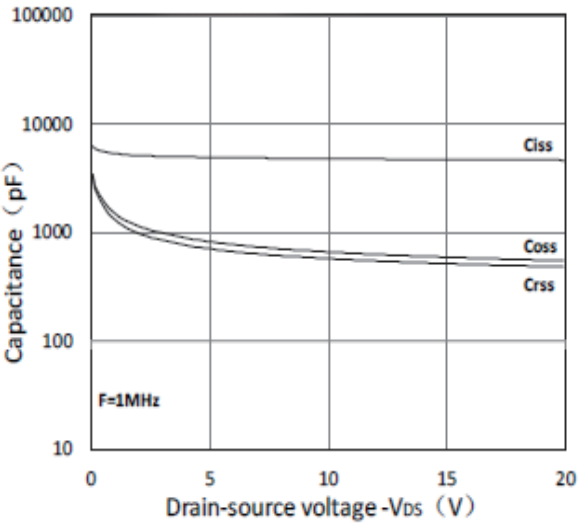


Figure 8: Gate Charge Characteristics

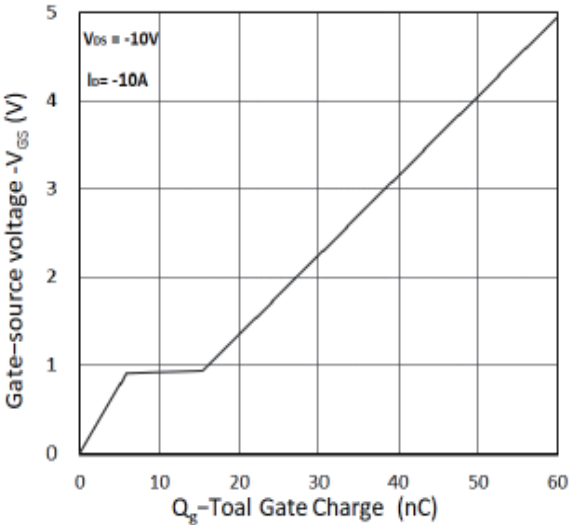


Figure 9: Power Dissipation

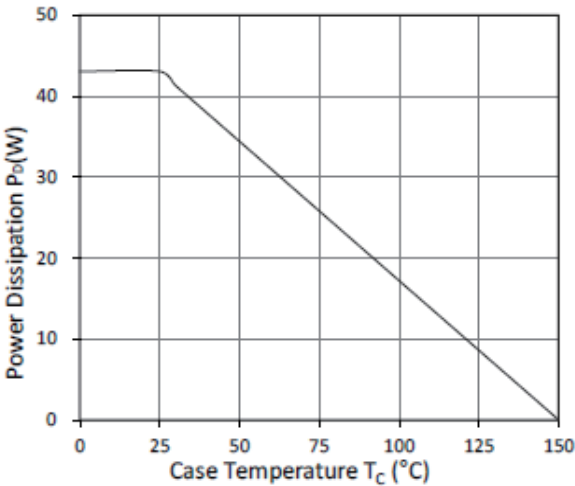


Figure 10: Safe Operating Area

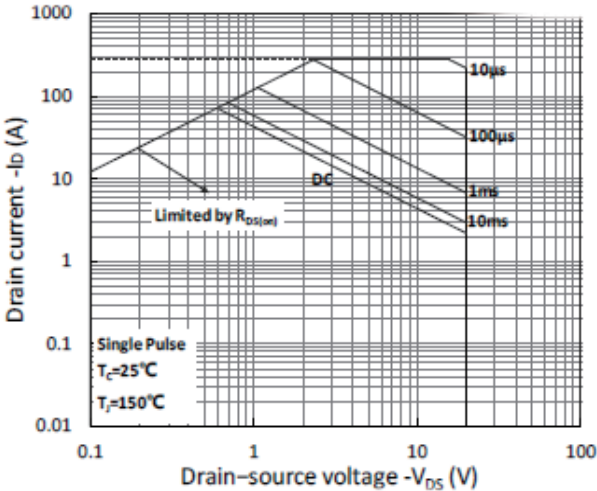
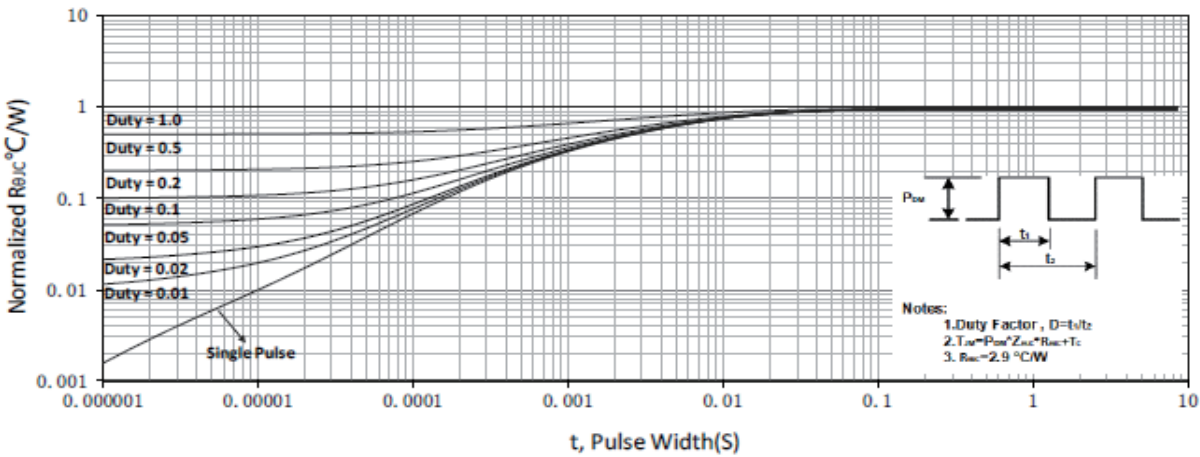


Figure 11: Normalized Maximum Transient Thermal Impedance

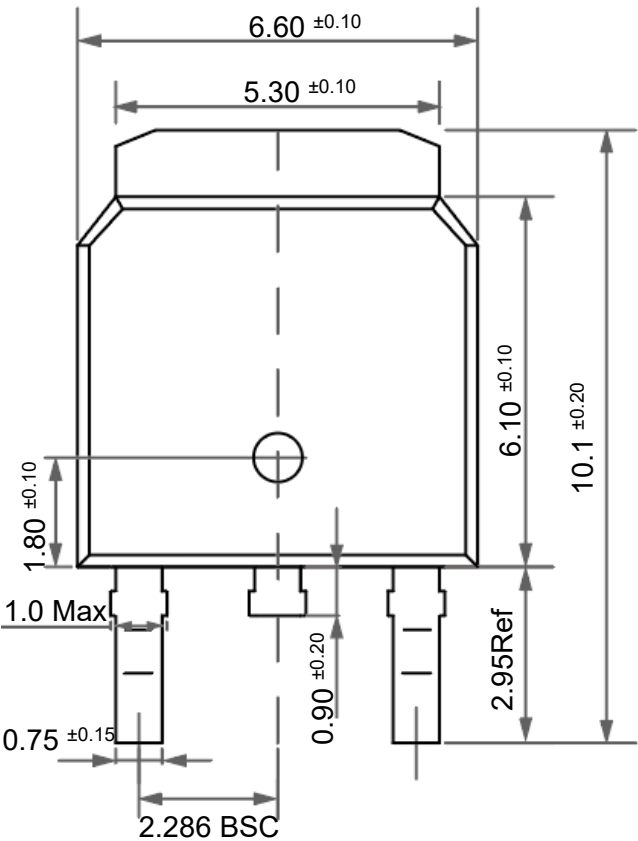




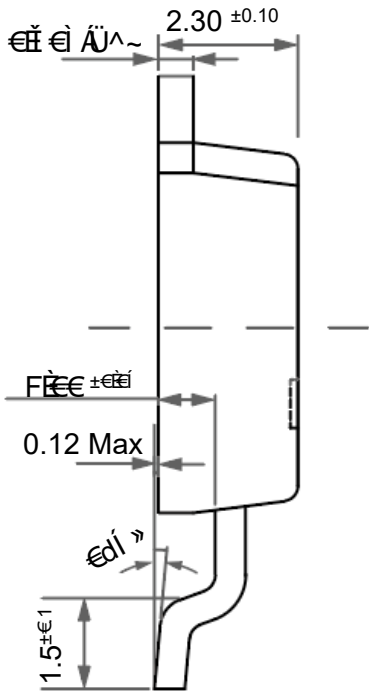
Package Outline

TO-252

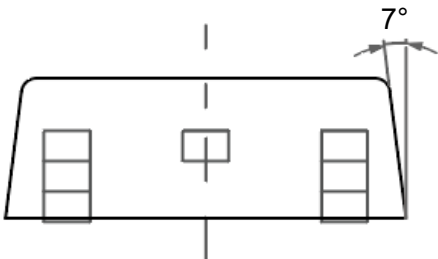
Dimensions in mm



Front View



Side View



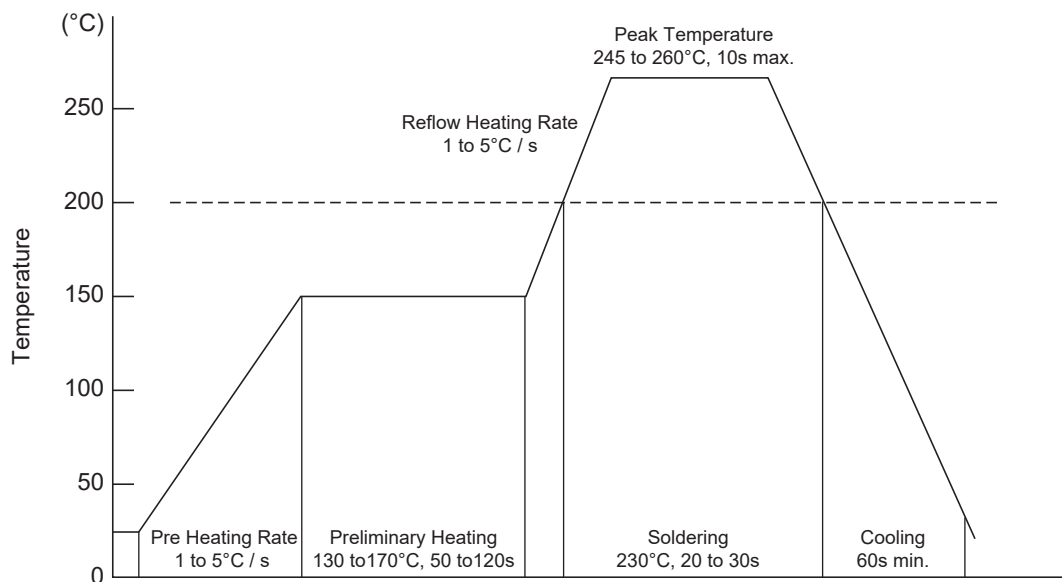
Bottom View

Ordering Information

Device	Package	Shipping
TN80P20TE	TO-252	2,500PCS/Reel&13inches

## Conditions of Soldering and Storage

### ◆ Recommended condition of reflow soldering



Recommended peak temperature is over 245°C. If peak temperature is below 245°C, you may adjust the following parameters:

- Time length of peak temperature (longer)
- Time length of soldering (longer)
- Thickness of solder paste (thicker)

### ◆ Conditions of hand soldering

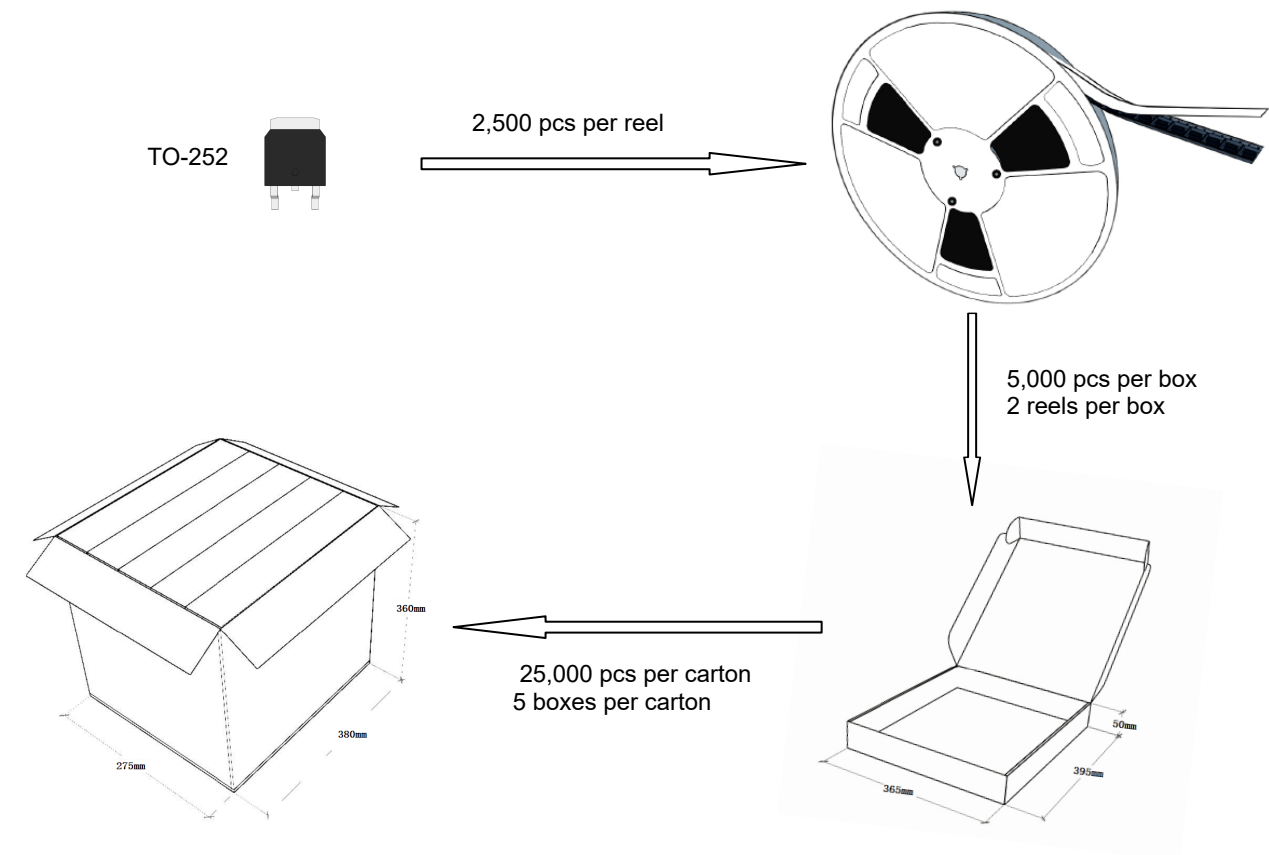
- Temperature: 300°C
- Time: 3s max.
- Times: one time

### ◆ Storage conditions

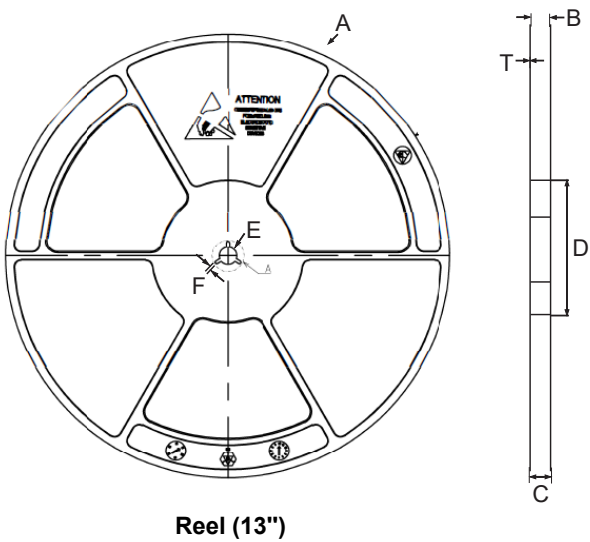
- **Temperature**  
5 to 40°C
- **Humidity**  
30 to 80% RH
- **Recommended period**  
One year after manufacturing

Package Specifications

- The method of packaging

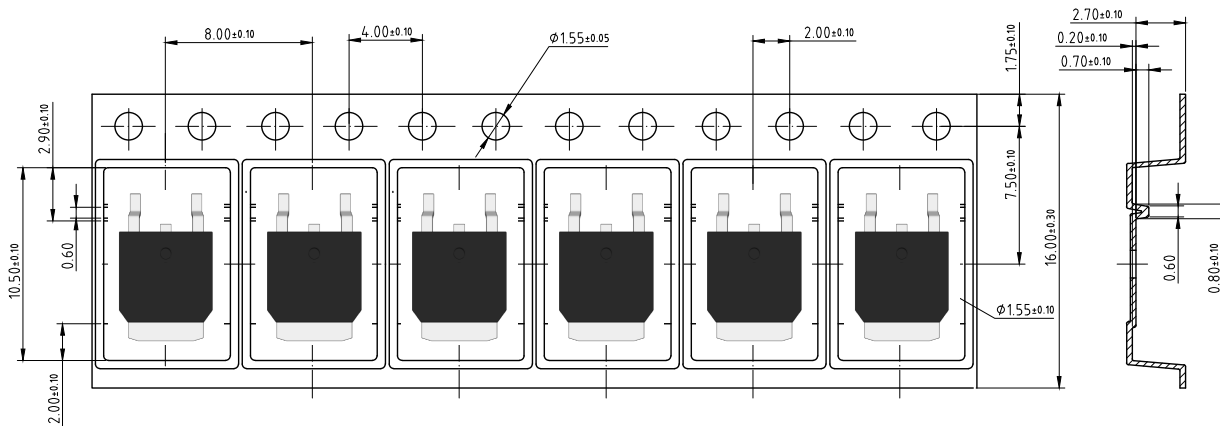


reel data



Symbol	Value(unit:mm)
A	$\Phi 330.2\pm 1$
B	$17\pm 0.5$
C	$21.2\pm 2$
D	$\Phi 100\pm 0.5$
E	$\Phi 13.4\pm 0.2$
F	$2.3\pm 0.2$
T	$2.1\pm 0.2$

◆ Embossed tape data



## Contact Information

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For additional information, please contact your local Sales Representative.

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### Product Specification Statement

The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.

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