

Product Summary

- $V_{DS} = 60V, I_D = 30A$
- $R_{DS(on)} < 33m\Omega @ V_{GS} = 10V$
- $R_{DS(on)} < 45m\Omega @ V_{GS} = 4.5V$

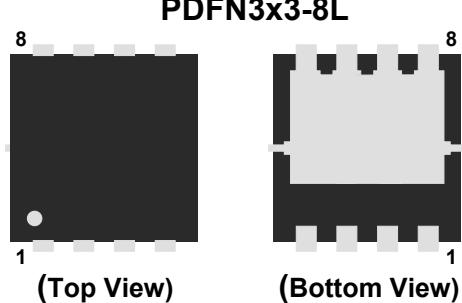
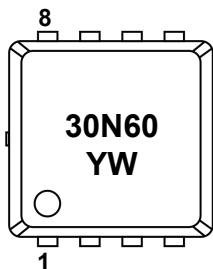
Features

- Advanced Trench Technology
- 100% Avalanche Tested
- RoHS Compliant
- Halogen and Antimony Free
- Moisture Sensitivity Level 3

Application

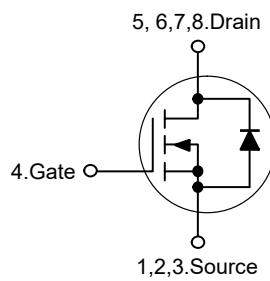
- Load Switch
- Battery Protection
- Uninterruptible Power Supply

Marking Code



Pin	Description
1,2,3	Source
4	Gate
5,6,7,8	Drain

Schematic Diagram



Absolute Maximum Ratings

Ratings at 25°C case temperature unless otherwise specified.

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $-V_{GS} @ -10V_1$	-26	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current, $-V_{GS} @ -10V_1$	-16	A
I_{DM}	Pulsed Drain Current ₂	-70	A
EAS	Single Pulse Avalanche Energy ₃	113	mJ
I_{AS}	Avalanche Current	47.6	A
$P_D @ T_c = 25^\circ C$	Total Power Dissipation ₄	5.2	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ₁	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ₁	---	4	°C/W

Electrical Characteristics

(T_C=25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-60	---	---	V
△BV _{DSS} /△T _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	-0.035	---	V/°C
R _{DSS(ON)}	Static Drain-Source On-Resistance ₂	V _{GS} =-10V, I _D =-10A	---	25	33	mΩ
		V _{GS} =-4.5V, I _D =-8A	---	33	45	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1.0	---	-2.5	V
△V _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	4.28	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-48V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =-48V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-12A	---	23	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	7	---	Ω
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-20V, V _{GS} =-4.5V, I _D =-12A	---	25	---	nC
Q _{gs}	Gate-Source Charge		---	6.7	---	
Q _{gd}	Gate-Drain Charge		---	5.5	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-15V, V _{GS} =-10V, R _G =3.3Ω, I _D =-1A	---	38	---	ns
T _r	Rise Time		---	23.6	---	
T _{d(off)}	Turn-Off Delay Time		---	100	---	
T _f	Fall Time		---	6.8	---	
C _{iss}	Input Capacitance	V _{DS} =-15V, V _{GS} =0V, f=1MHz	---	3635	---	pF
C _{oss}	Output Capacitance		---	224	---	
C _{rss}	Reverse Transfer Capacitance		---	141	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _s	Continuous Source Current _{1,5}	V _G =V _D =0V, Force Current	---	---	-26	A
I _{SM}	Pulsed Source Current _{2,5}		---	---	-70	A
V _{SD}	Diode Forward Voltage ₂	V _{GS} =0V, I _s =-1A, T _J =25°C	---	---	-1	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=-25V,V_{GS}=-10V,L=0.1mH,I_{AS}=-47.6A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

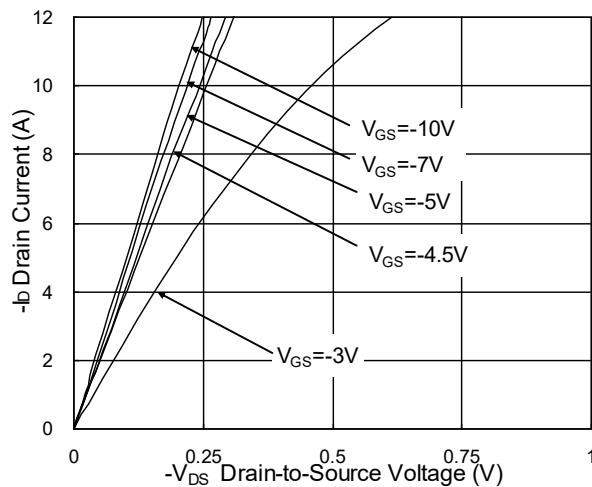


Fig.1 Typical Output Characteristics

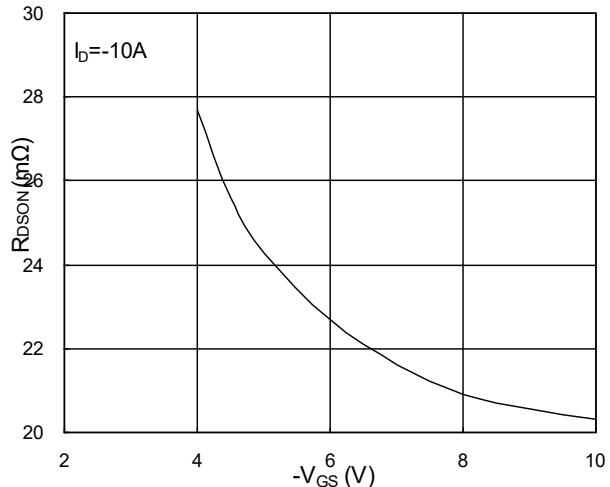


Fig.2 On-Resistance v.s Gate-Source

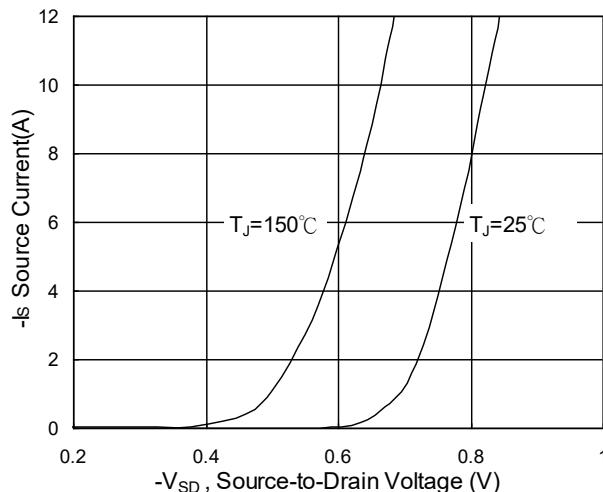


Fig.3 Forward Characteristics Of Reverse

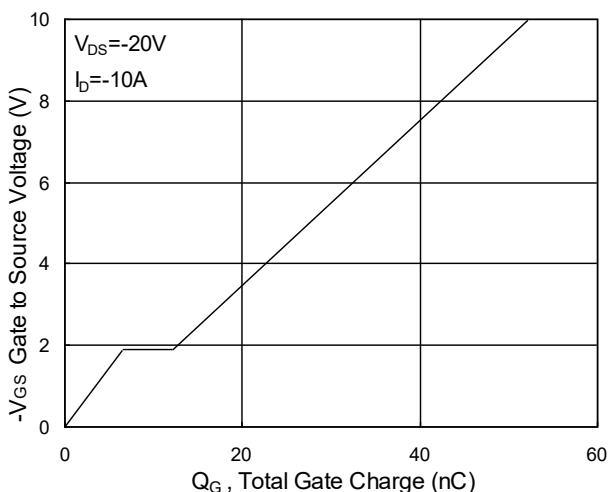


Fig.4 Gate-Charge Characteristics

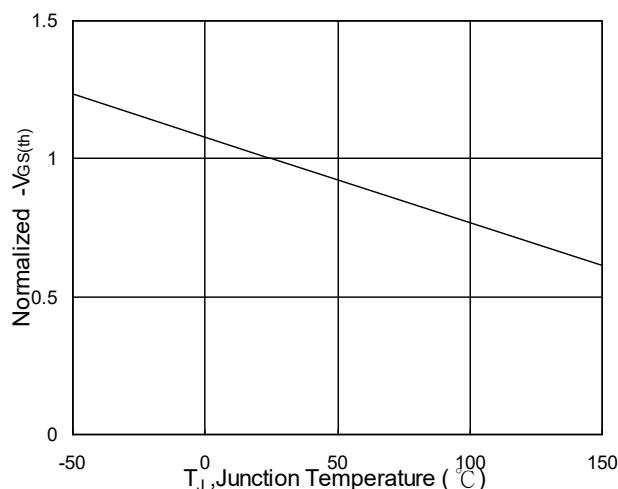


Fig.5 Normalized V_GS(th) v.s T_J

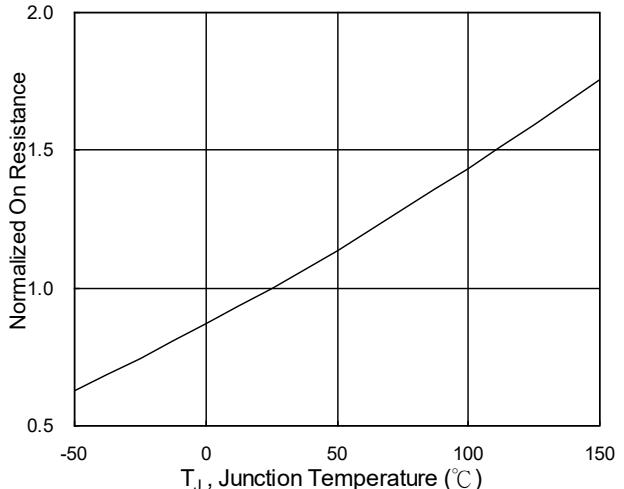


Fig.6 Normalized R_DSON v.s T_J

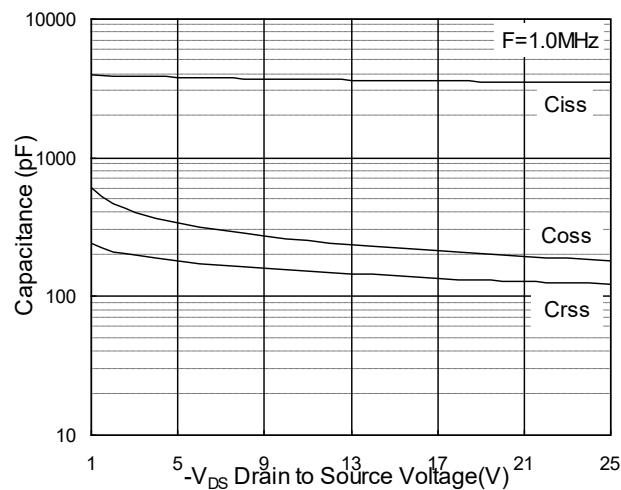


Fig.7 Capacitance

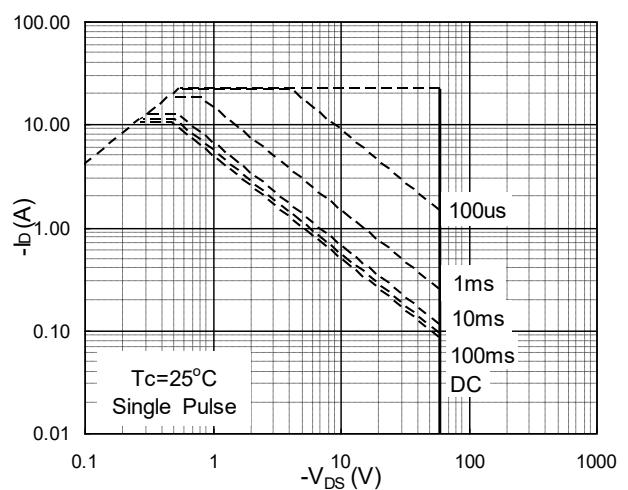


Fig.8 Safe Operating Area

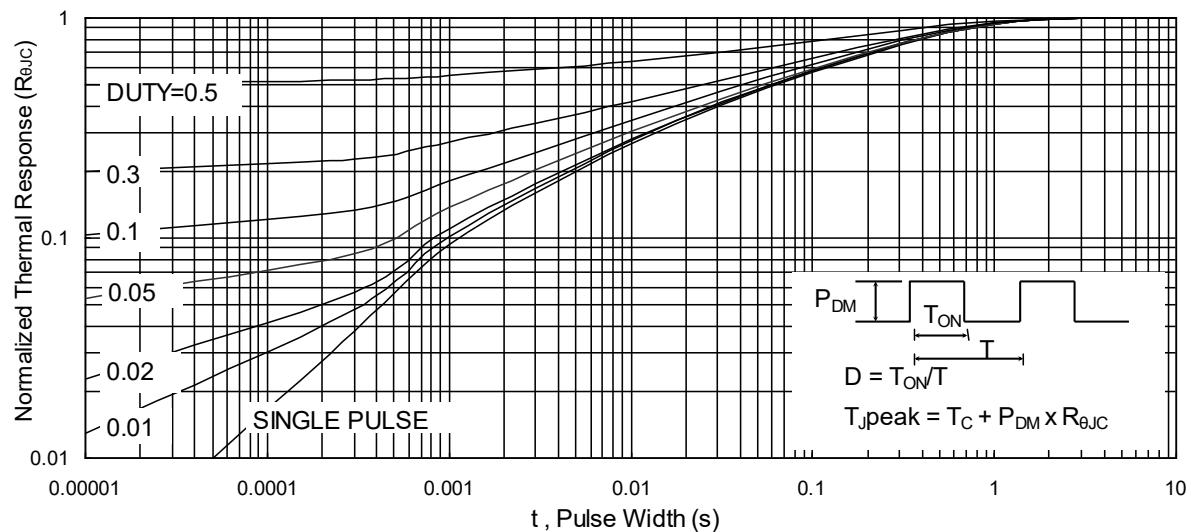


Fig.9 Normalized Maximum Transient Thermal Impedance

Test Circuit

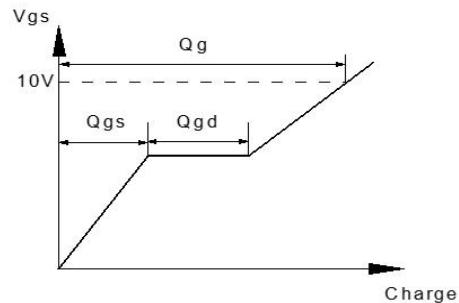
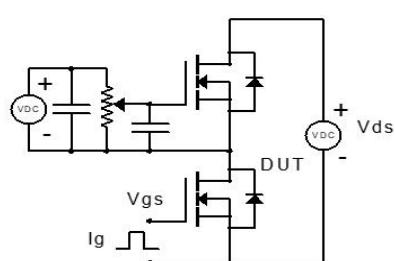


Figure 1: Gate Charge Test Circuit & Waveform

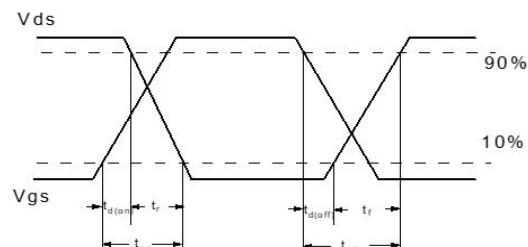
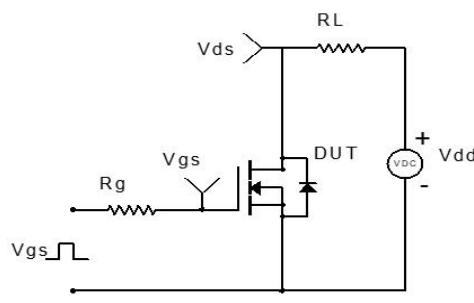


Figure 2: Resistive Switching Test Circuit & Waveform

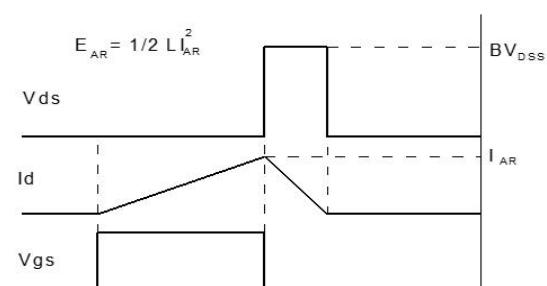
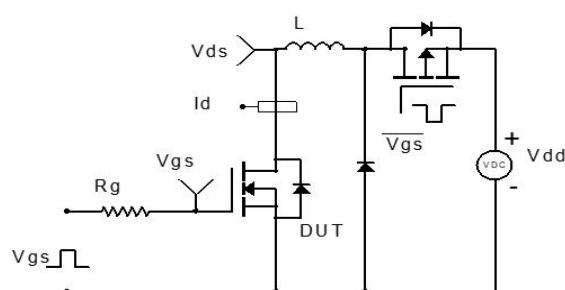


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

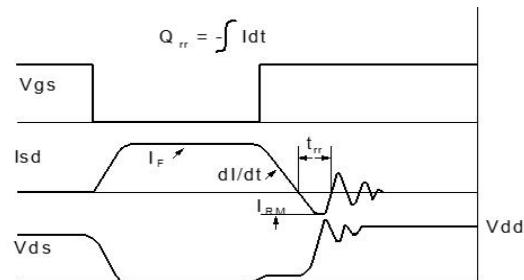
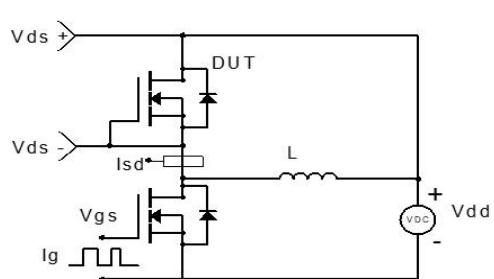
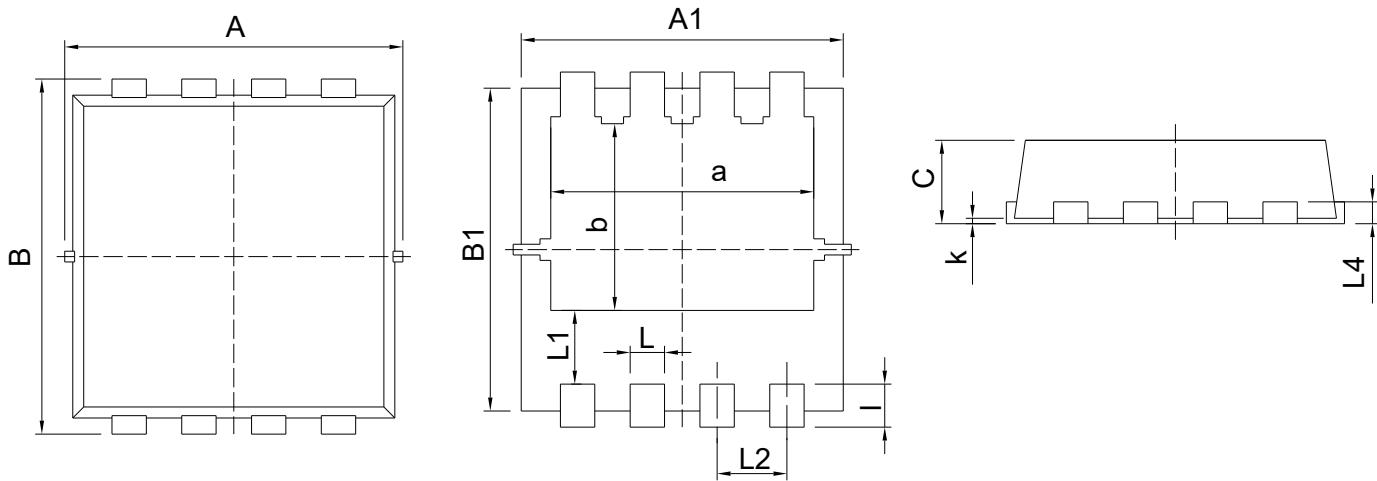


Figure 4: Diode Recovery Test Circuit & Waveform

Package Outline

PDFN3x3-8L Dimensions in mm



Symbol	Dimensions		Symbol	Dimensions	
	Min.	Max.		Min.	Max.
A	3.2	3.4	L2	0.55	0.75
A1	3.1	3.2	L4	0.14	0.20
B	3.2	3.4	a	2.35	2.55
B1	2.95	3.05	b	1.635	1.835
C	0.75	0.85	k	0.0	0.05
L	0.25	0.35	l	0.3	0.5
L1	-	0.75			

Contact Information

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For additional information, please contact your local Sales Representative.



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