



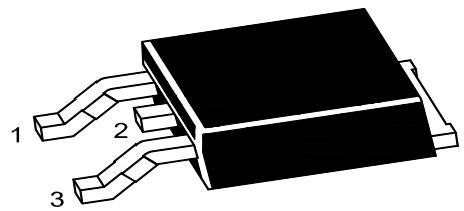
TN20H18NTE

N-Channel Enhancement Mode Power MOSFET

## Features

- Fast Switching
- Low Reverse transfer capacitances
- Low gate charge and low  $R_{DS(on)}$
- $V_{DS} = 200V, I_D = 18A$
- $R_{DS(on)} < 0.18\Omega @ V_{GS} = 10V$

TO-252

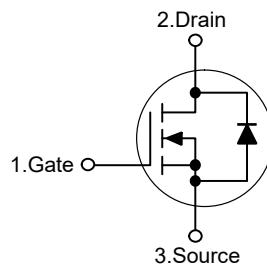


1. Gate 2. Drain 3. Source

## Applications

- Power switch circuit of PC Power.

## Schematic Diagram



## Absolute Maximum Ratings

Ratings at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	200	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Drain Current-Continuous	$I_D$	18	A
Drain Current-Pulsed <sup>Note1</sup>	$I_{DM}$	72	A
Single pulse avalanche energy <sup>Note4</sup>	$E_{AS}$	950	mJ
Avalanche energy, Repetitive <sup>Note1</sup>	$E_{AR}$	90	mJ
Avalanche Current <sup>Note1</sup>	$I_{AR}$	4.2	A
Maximum Power Dissipation	$P_D$	156	W
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-55 to +150	°C

## Thermal Characteristics

Thermal Resistance, Junction-to-Ambient <sup>Note2</sup>	$R_{\theta JA}$	62.5	°C/W
Maximum Junction-to-Case <sup>Note2</sup>	$R_{\theta JC}$	0.8	°C/W

**Electrical Characteristics**

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	200	--	--	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =200V, V <sub>GS</sub> =0V	--	--	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	--	--	±100	nA
Gate Threshold Voltage <sup>Note3</sup>	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2	--	4	V
Drain-Source On-Resistance <sup>Note3</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =10.8A	--	0.12	0.18	Ω
Forward Transconductance <sup>Note3</sup>	g <sub>FS</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =18A	--	18	--	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz	--	1140	--	pF
Output Capacitance	C <sub>oss</sub>		--	180	--	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		--	25	--	pF
<b>Switching Characteristics</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =100V, V <sub>GS</sub> =10V, I <sub>D</sub> =18A R <sub>G</sub> =2.4Ω	--	11	--	nS
Turn-on Rise Time	t <sub>r</sub>		--	13	--	nS
Turn-off Delay Time	t <sub>d(off)</sub>		--	25	--	nS
Turn-off Fall Time	t <sub>f</sub>		--	7	--	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DD</sub> =100V, V <sub>GS</sub> =10V, I <sub>D</sub> =18A	--	24	--	nC
Gate-Source Charge	Q <sub>gs</sub>		--	7.5	--	nC
Gate-Drain Charge	Q <sub>gd</sub>		--	9.5	--	nC
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>Note3</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>s</sub> =18A	--	--	1.5	V
Diode Forward Current <sup>Note2</sup>	I <sub>s</sub>		--	--	18	A

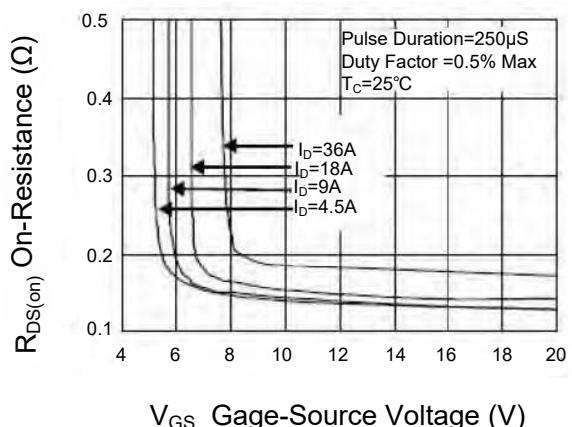
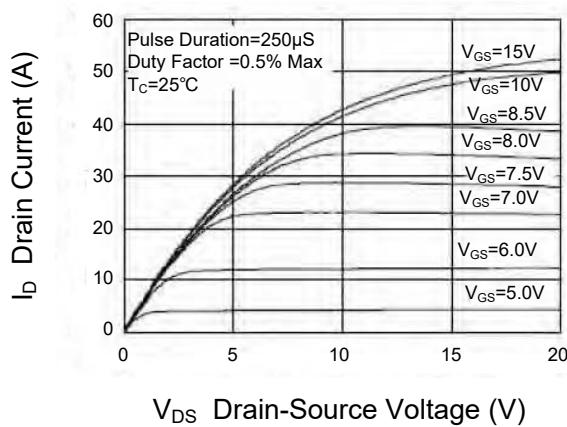
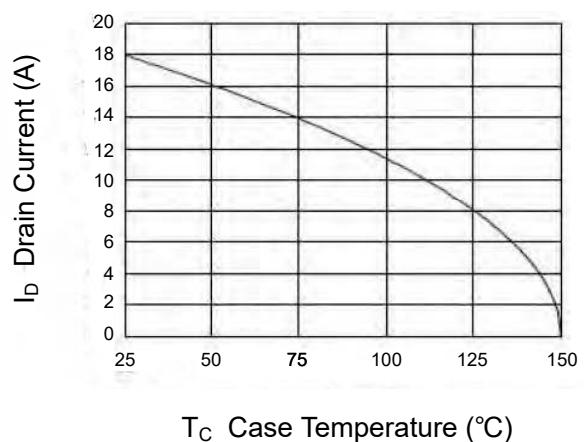
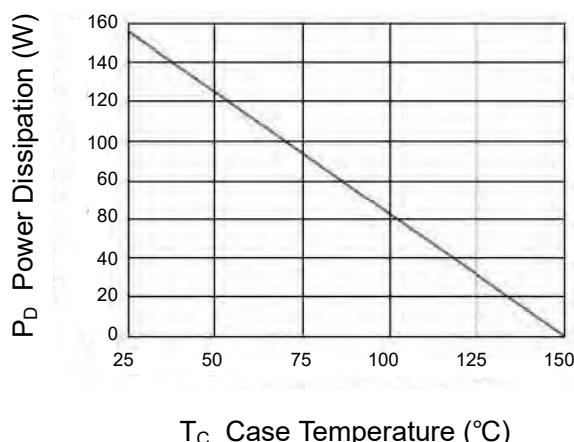
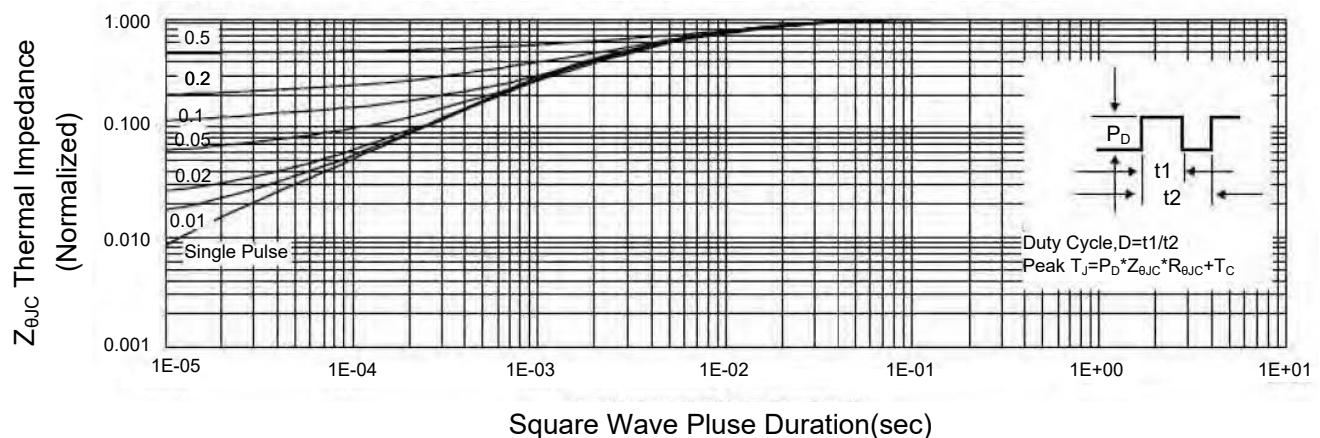
Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

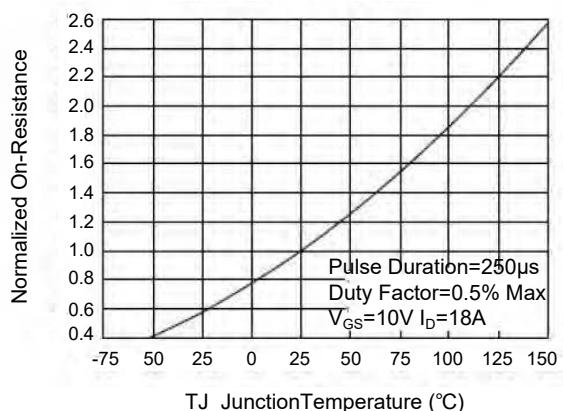
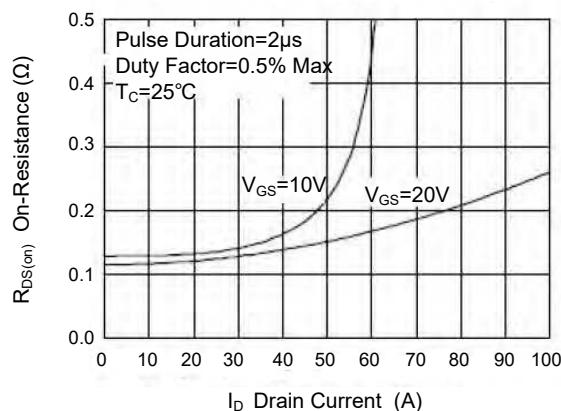
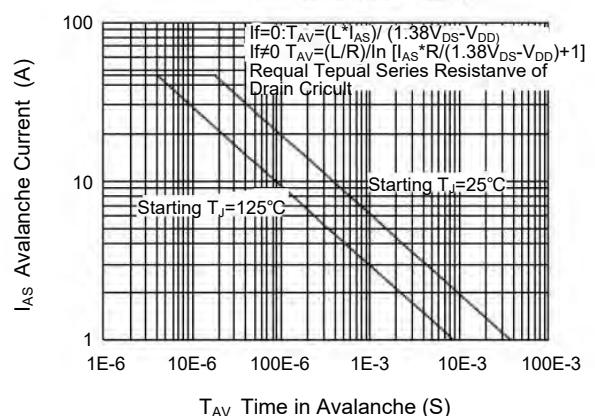
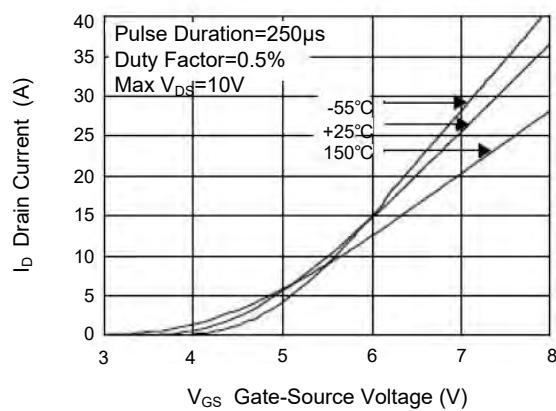
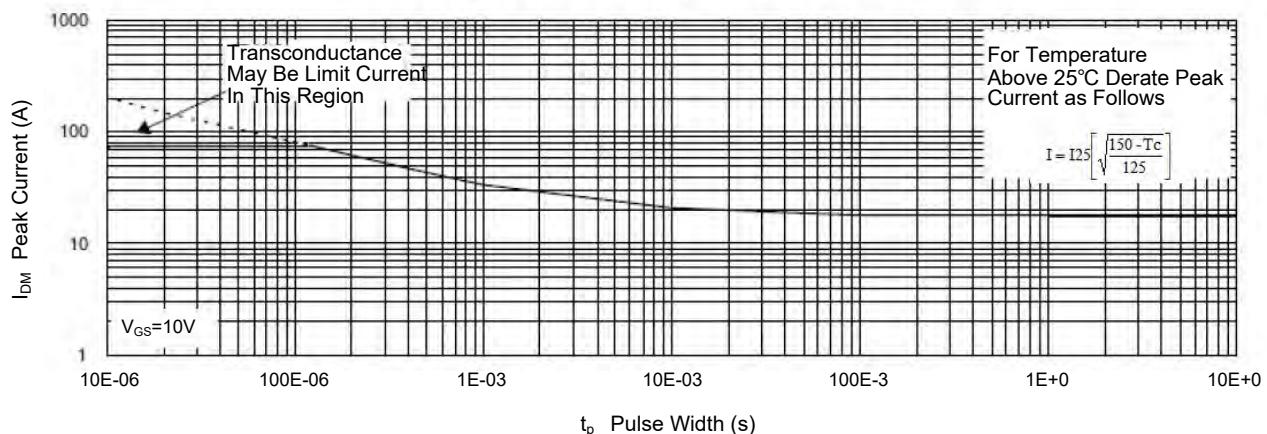
2. Surface Mounted on FR4 Board, t ≤ 10 sec.

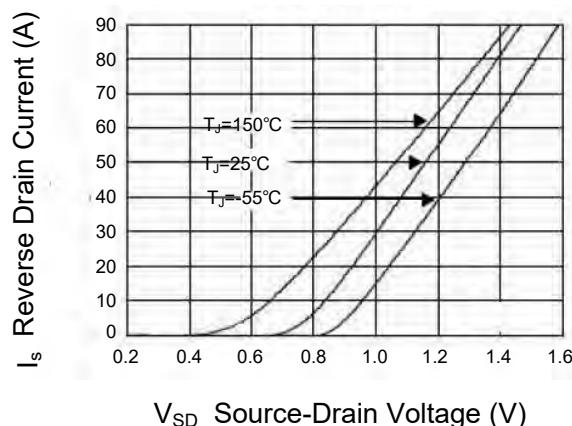
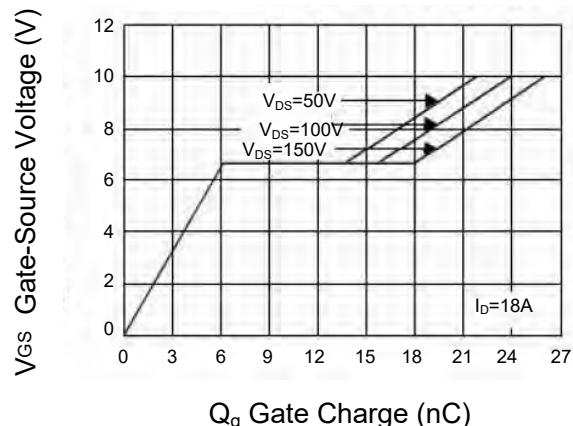
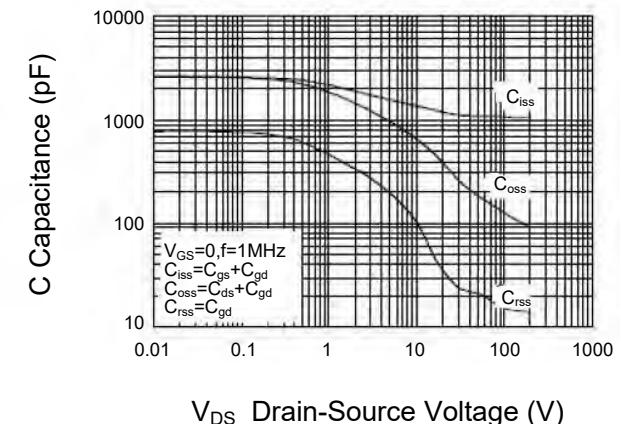
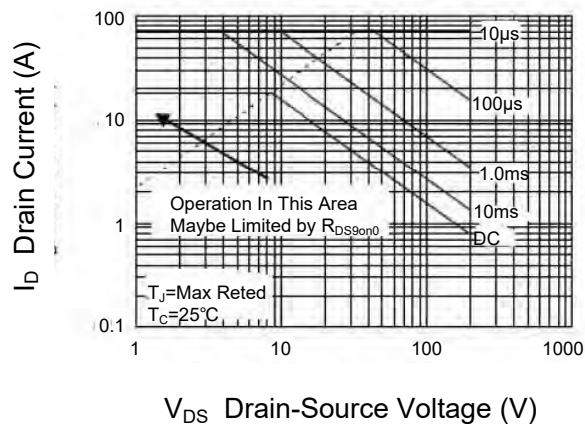
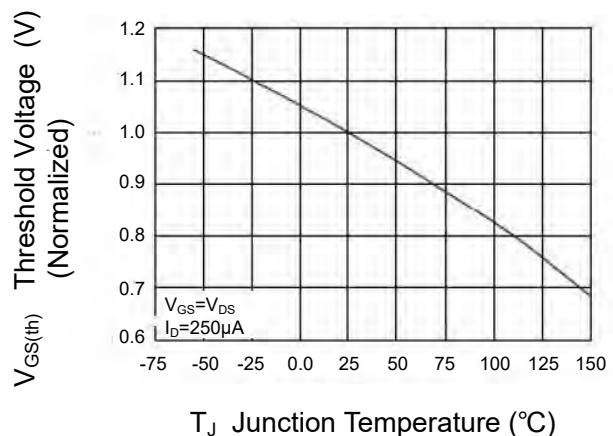
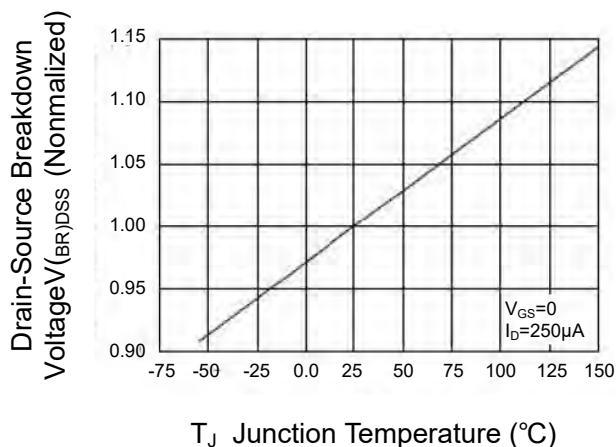
3. Pulse Test: Pulse width≤380μs, duty cycle≤2%

4. E<sub>AS</sub> condition : L=10.0mH, I<sub>D</sub>=13.8A, Start T<sub>J</sub>=25°C

## Typical Characteristic Curves



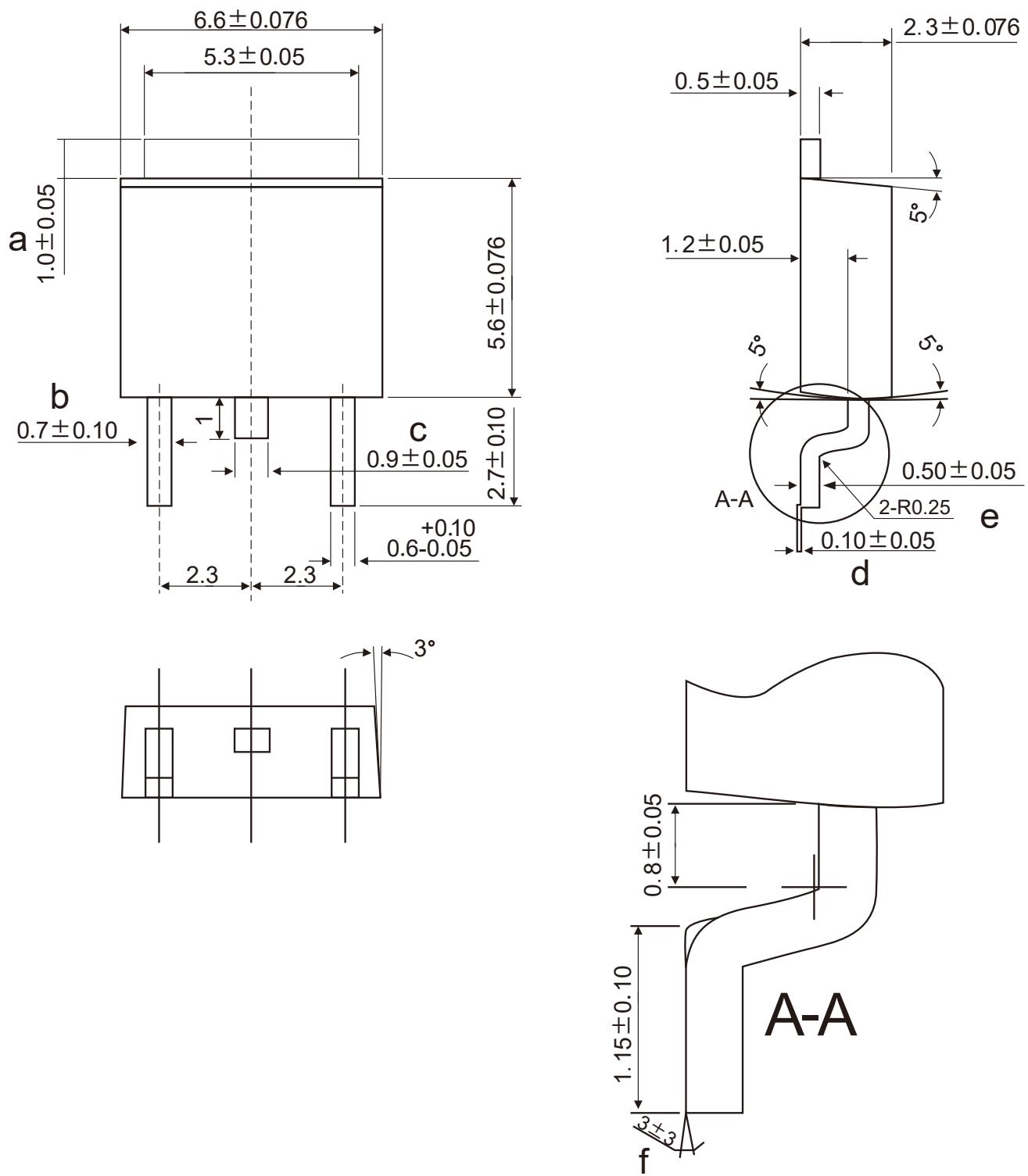




**Package Outline**

TO-252

Dimensions in mm



## Contact Information

TANI website: <http://www.tanisemi.com> Email:tani@tanisemi.com

For additional information, please contact your local Sales Representative.



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### Product Specification Statement

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