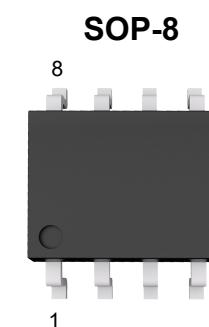




Features

- High density cell design for ultra low $R_{DS(on)}$
- Excellent package for good heat dissipation
- $V_{DS} = -30V, I_D = -12A$
- $R_{DS(on)} < 10.5m\Omega$ @ $V_{GS} = -10V$

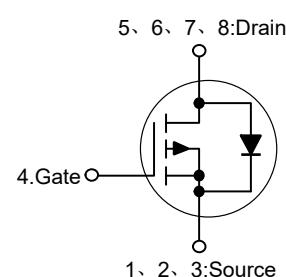


Marking code: 4407

Applications

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

Schematic Diagram



Absolute Maximum Ratings

Ratings at 25°C ambient temperature unless otherwise specified.

| Parameter | Symbol | Value | Unit |
|---------------------------------------|-----------|-------------|------|
| Drain-Source Voltage | $-V_{DS}$ | 30 | V |
| Gate-Source Voltage | $-V_{GS}$ | ± 20 | V |
| Drain Current-Continuous | $-I_D$ | 12 | A |
| Drain Current-Pulsed ^{Note1} | $-I_{DM}$ | 48 | A |
| Maximum Power Dissipation | P_D | 3 | W |
| Junction Temperature | T_J | 150 | °C |
| Storage Temperature Range | T_{STG} | -55 to +150 | °C |

Thermal Characteristics

| | | | |
|---|-----------|------|------|
| Maximum Junction-to-Case ^{Note2} | R_{eJC} | 41.7 | °C/W |
|---|-----------|------|------|



Electrical Characteristics

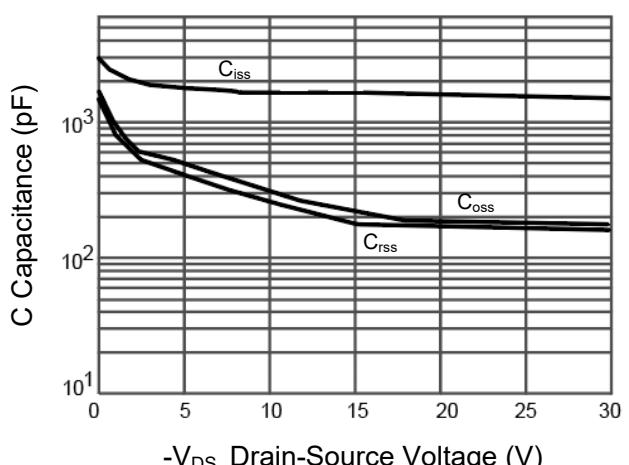
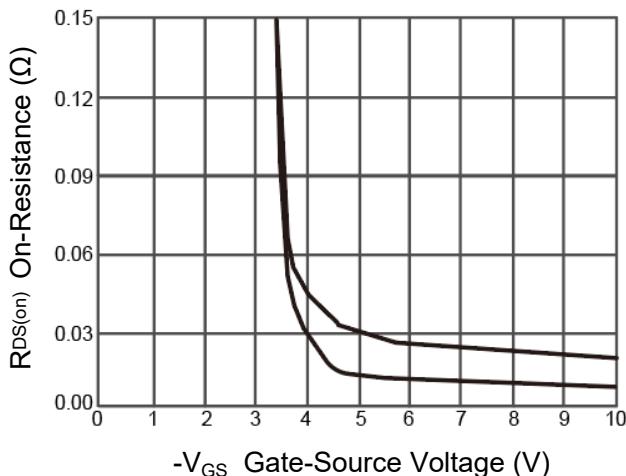
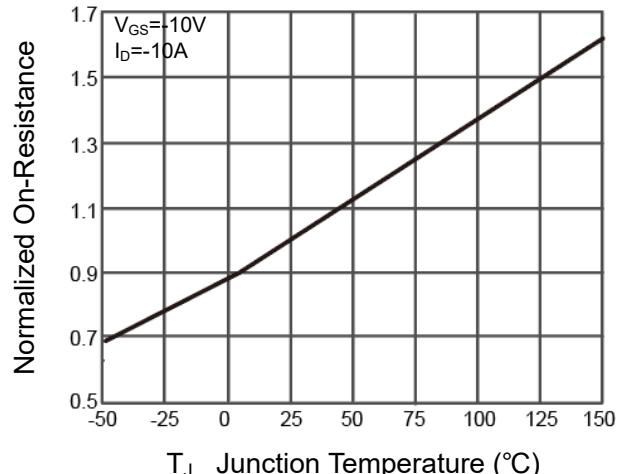
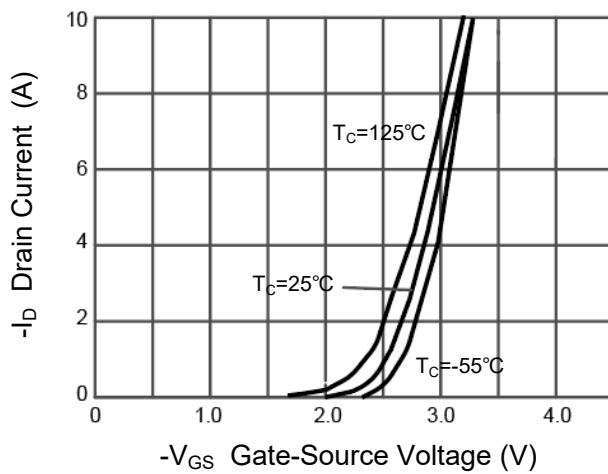
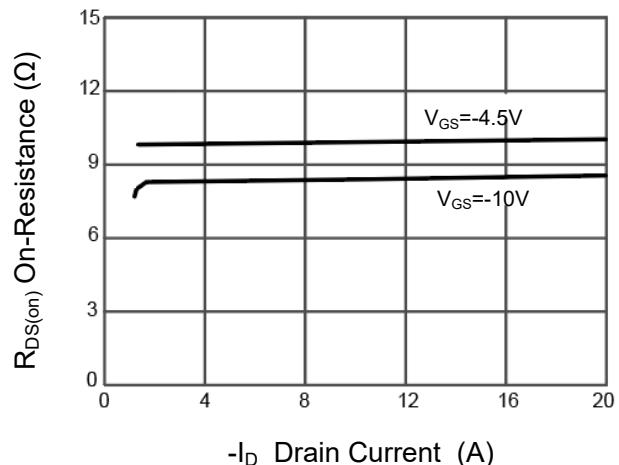
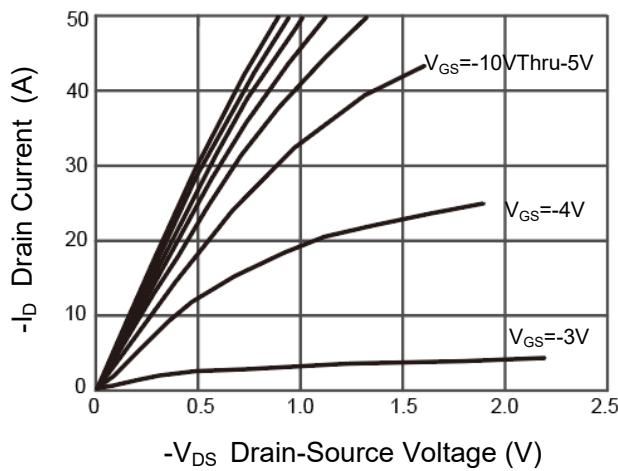
(Ta=25°C unless otherwise specified)

| Parameter | Symbol | Test Condition | Min. | Typ. | Max. | Unit |
|---|-----------------------|--|------|------|------|------|
| Static Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | -V _{(BR)DSS} | V _{GS} =0V, I _D =-250μA | 30 | -- | -- | V |
| Zero Gate Voltage Drain Current | -I _{DSS} | V _{DS} =-30V, V _{GS} =0V | -- | -- | 1 | μA |
| Gate-Body Leakage Current | I _{GSS} | V _{GS} =±20V, V _{DS} =0V | -- | -- | ±100 | nA |
| Gate Threshold Voltage ^{Note3} | -V _{GS(th)} | V _{DS} =V _{GS} , I _D =-250μA | 1 | -- | 3 | V |
| Drain-Source On-Resistance ^{Note3} | R _{DS(on)} | V _{GS} =-10V, I _D =-12A | -- | 8.5 | 10.5 | mΩ |
| | | V _{GS} =-4.5V, I _D =-10A | -- | 10 | 12 | mΩ |
| Forward Transconductance ^{Note3} | g _{FS} | V _{DS} =-5V, I _D =-10A | 20 | -- | -- | S |
| Dynamic Characteristics | | | | | | |
| Input Capacitance | C _{iss} | V _{DS} =-15V, V _{GS} =0V, f=1MHz | -- | 1800 | -- | pF |
| Output Capacitance | C _{oss} | | -- | 220 | -- | pF |
| Reverse Transfer Capacitance | C _{rss} | | -- | 180 | -- | pF |
| Switching Characteristics | | | | | | |
| Turn-on Delay Time | t _{d(on)} | V _{DD} =-15V, V _{GS} =-10V I _D =-10A, R _{GEN} =3Ω | -- | 10 | -- | nS |
| Turn-on Rise Time | t _r | | -- | 9 | -- | nS |
| Turn-off Delay Time | t _{d(off)} | | -- | 26 | -- | nS |
| Turn-off Fall Time | t _f | | -- | 11 | -- | nS |
| Total Gate Charge | Q _g | V _{DS} =-15V, V _{GS} =-10V I _D =-10A | -- | 25 | -- | nC |
| Gate-Source Charge | Q _{gs} | | -- | 4 | -- | nC |
| Gate-Drain Charge | Q _{gd} | | -- | 6 | -- | nC |
| Source-Drain Diode Characteristics | | | | | | |
| Diode Forward Voltage ^{Note3} | -V _{SD} | V _{GS} =0V, I _S =-12A | -- | -- | 1.5 | V |
| Diode Forward Current ^{Note2} | -I _S | | -- | -- | 12 | A |

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, t ≤ 10 sec.

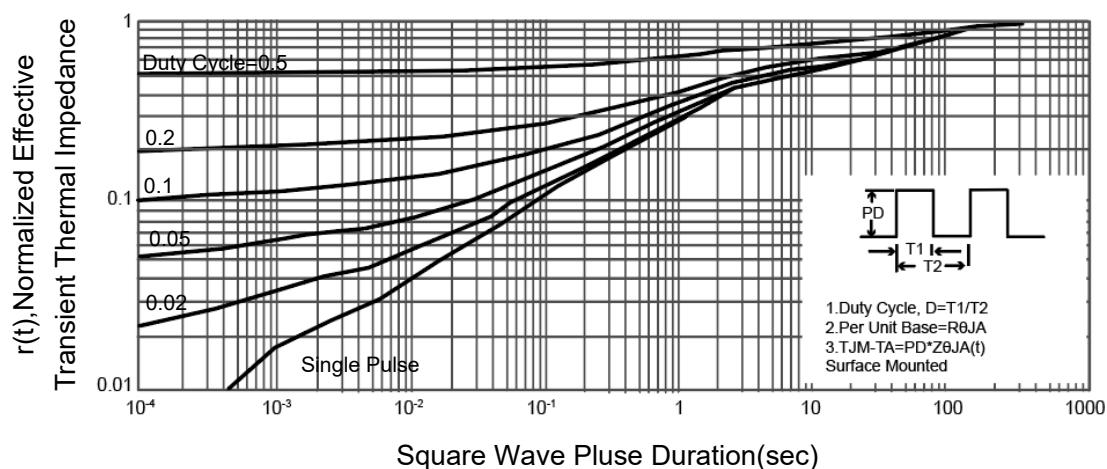
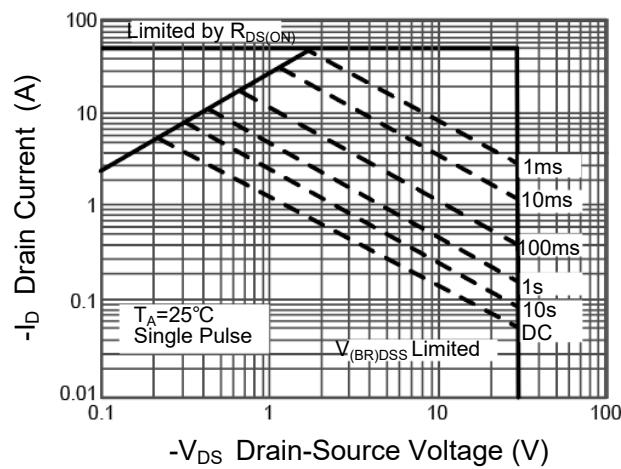
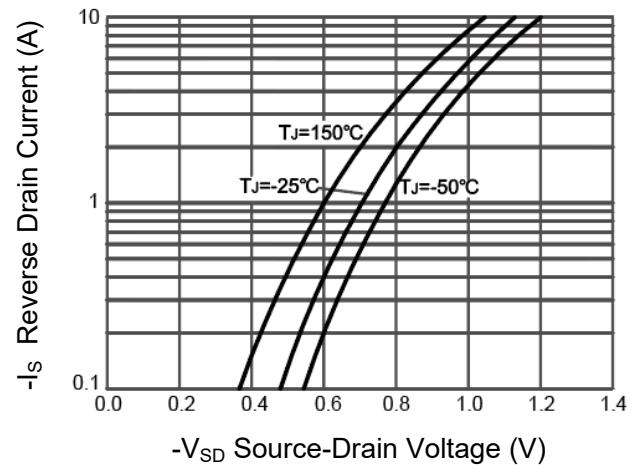
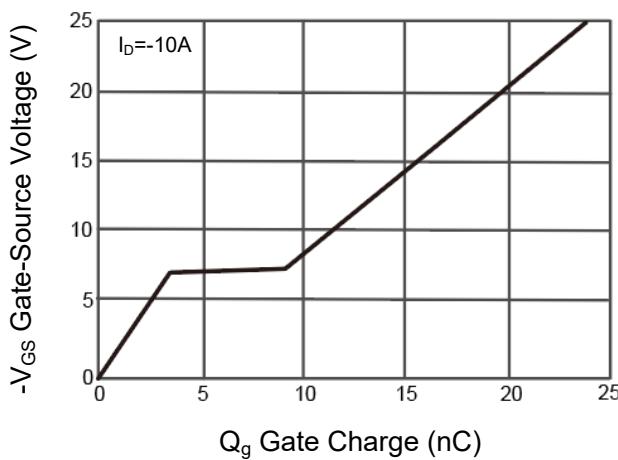
3. Pulse Test: Pulse width≤300μs, duty cycle≤2%

Typical Characteristic Curves




TN12P30PA

P-Channel Enhancement Mode Power MOSFET

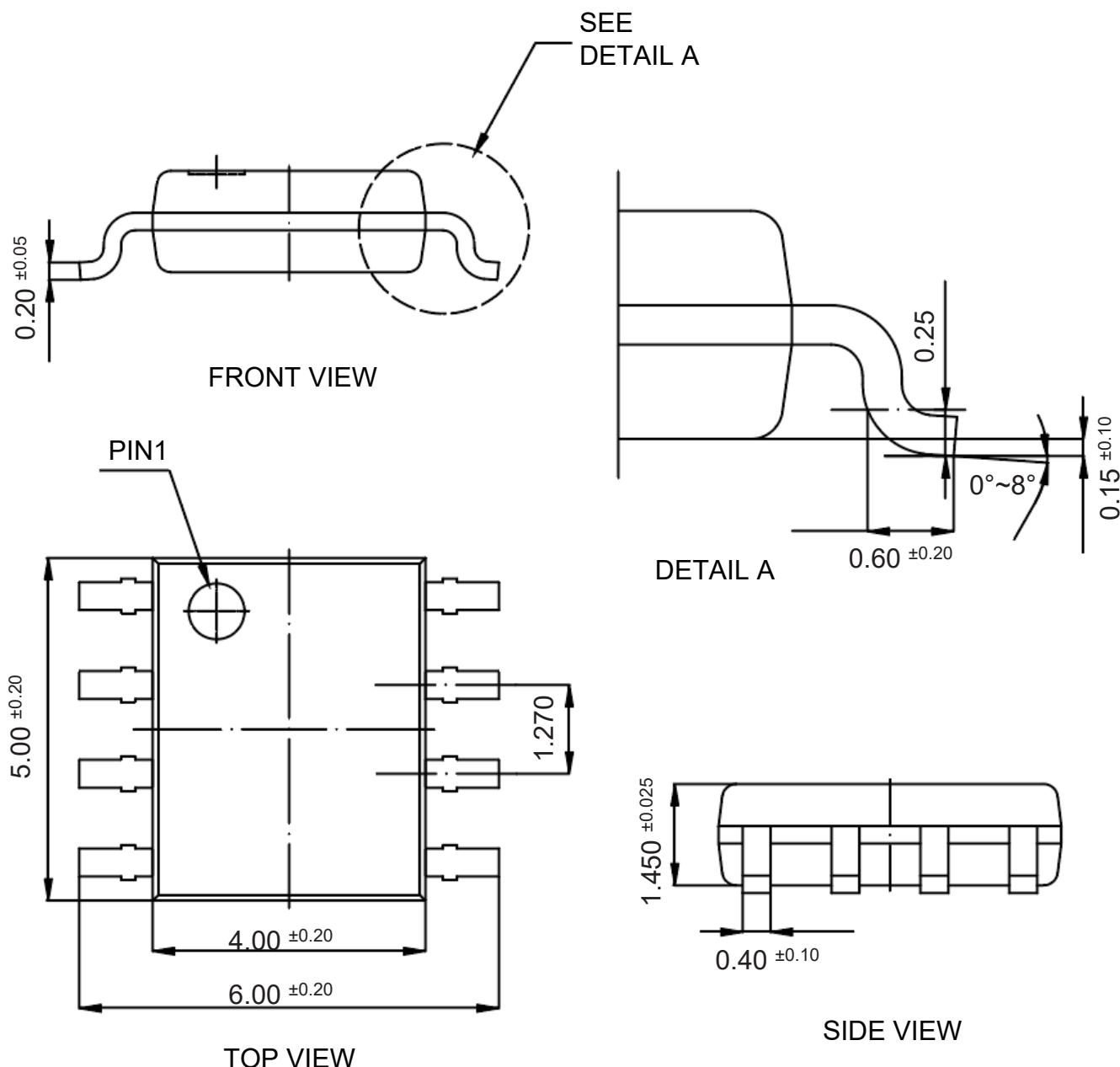




Package Outline

SOP-8

Dimensions in mm



Ordering Information

| Device | Package | Shipping |
|-----------|---------|------------------------|
| TN12P30PA | SOP-8 | 4,000PCS/Reel&13inches |



Contact Information

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For additional information, please contact your local Sales Representative.



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