

# TN7002KNDC

#### **N-Channel Enhancement Mode Power MOSFET**

#### **Features**

- VDS= 60V,ID= 0.3A RDS(on) $\leq$ 3 $\Omega$  @V<sub>GS</sub>= 10V
- Halogen and Antimony Free
- Moisture Sensitivity Level 1
- RoHS Compliant

Pin	Description	1 2
1	Gate	
2	Source	
3	Drain	3
		(Bottom View

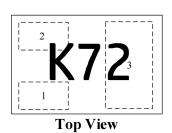
#### **Mechanical Characteristics**

- Package:DFN x0.6-3L
- Packaging: Tape and Reel per EIA 481
- Marking : Making Code
- RoHS Compliant

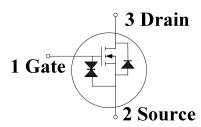
### **Applications**

- Load Switch
- PWM applications
- Power Management

## Marking: Making Code



## **Schematic Diagram**



### Absolute Maximum Rating(Ratings at 25 °C ambient temperature unless otherwise specified.)

Parameter	Symbols	Value	Unit
Drain-Source Voltage	$V_{ m DS}$	60	V
Gate-Source Voltage	$V_{GS}$	±20	V
Drain Current-Continuous	$I_D$	0.3	A
Drain Current-Pulsed Note1	$I_{DM}$	0.8	A
Junction Temperature	$P_{\mathrm{D}}$	0.3	W
Maximum Power Dissipation	$T_{\mathrm{J}}$	150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

## **Thermal Characteristics**

## Electrical Characteristics(Tc=25°C Unless otherwise specified)

Parameter	Symbols	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}=0V,I_{D}=250\mu A$	60			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}$ =60V, $V_{GS}$ =0V			1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}=\pm20V, V_{DS}=0V$			±10	μΑ
Gate Threshold Voltage Note3	V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_{D}=250\mu A$	1	1.4	2.5	V
Drain-Source On-Resistance Note3	D	$V_{GS}=10V, I_{D}=0.5A$		1.9	3	Ω
	R <sub>DS(ON)</sub>	$V_{GS}=2.5V, I_{D}=0.3A$		2.2	4	Ω
Forward Transconductance Note3	gfs	V <sub>DS</sub> =10V,I <sub>D</sub> =0.2A	0.1			S
Dynamic Characteristics						
Input Capacitance	Ciss			21		pF
Output Capacitance	Coss	$V_{DS} = 25V, V_{GS} = 0V, f = 1MHz$		11		pF
Reverse Transfer Capacitance	$C_{rss}$			4.2		pF
Total Gate Charge	Qg	$V_{DS}=10V, I_{D}=0.3A, V_{GS}=4.5V$		1.7		nC
Switching Characteristics						
Turn-on Delay Time	t <sub>d(on)</sub>			10		nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}=30V, I_{D}=0.2\Omega,$		50		nS
Turn-off Delay Time	t <sub>d(off)</sub>	$V_{GEN}=10V,R_{G}=10\Omega$		17		nS
Turn-off Fall Time	$t_{\mathrm{f}}$			10		nS
Source-Drain Diode Characteristics						
Diode Forward Voltage Note3	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =0.2A			1.2	V
Diode Forward Current Note2	Is				0.3	A

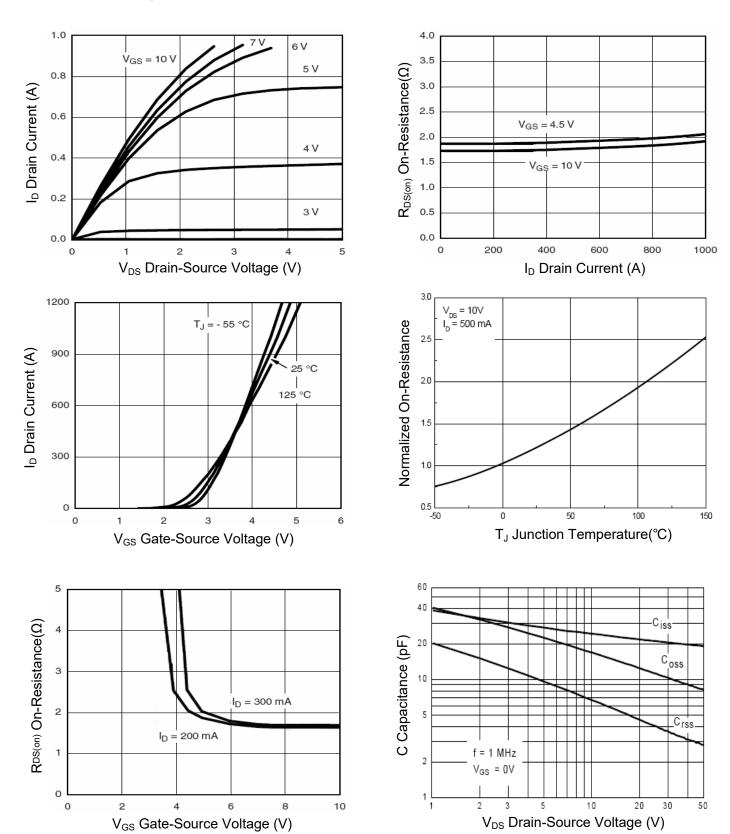
#### Notes:

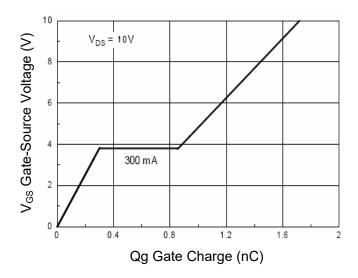
- Repetitive Rating: Pulse width limited by maximum junction temperature.
- Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.
- Pulse Test: Pulse width≤300 

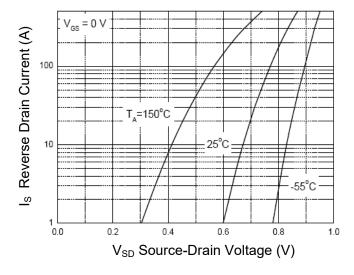
  µ s, duty cycle≤2%.

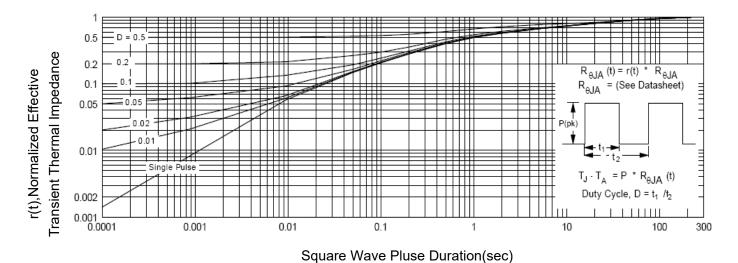
# **Typical Performance**

At  $T_J$ = 25°C, unless specified otherwise









### **Package Information**

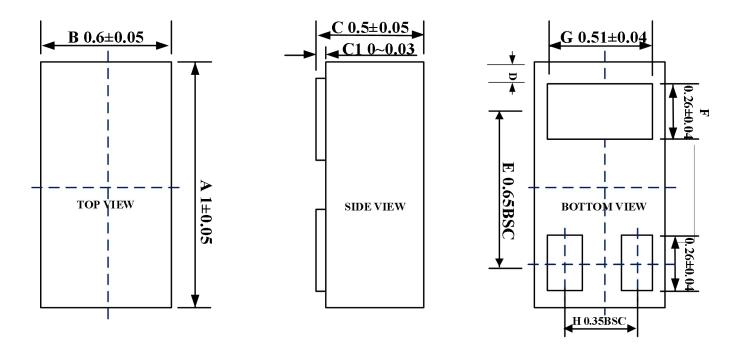
Package Type	Description	Quantity (pcs)	Standard
DFNx0.6-3L	Reel -7" tape	10000	EIA-481

## **Revision history**

Date	Revision	Changes
18-Nov-2019	A	First release
22-Nov-2024	В	Updated title, features and description on cover page.
		Document status promoted from preliminary to production data.
09-Jan-2025	С	Updated the naming model.
		Minor text changes.

### **Outline Drawing**

DFNx0.6-3L(Dimensions in mm)



#### **Contact Information**

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For additional information, please contact your local Sales Representative.



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