

# TN12P20DF

### Features

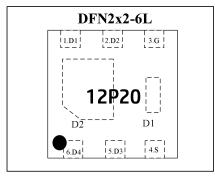
- $V_{DS} = -20V, I_D = -12A$  $R_{DS}(on) < 22m\Omega @V_{GS} = -4.5V$
- Small Surface Mount Package
- Low gate charge Low R<sub>DS(ON)</sub>
- RoHS Compliant

# P-Channel Enhancement Mode Power MOSFET DFN2x2-6L 1.Drain1 2.Drain2 3.Gate D S D S 0.Drain4 5.Drain3 4.Source (Top View) (Bottom View)

# **Mechanical Characteristics**

- Package:DFN 2x2-6L
- Packaging: Tape and Reel per EIA 481
- Marking : Making Code

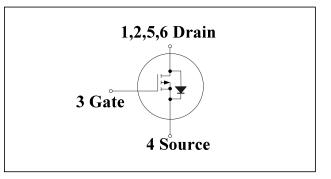
### Marking : Making Code



### Applications

- Load Switch
- PWM application

#### **Schematic Diagram**



Absolute Maximum Rating(Ratings at 25 °C ambient temperature unless otherwise specified.)

Parameter	Symbols	Value	Unit
Drain-Source Voltage	-V <sub>DS</sub>	20	V
Gate-Source Voltage	V <sub>GS</sub>	±12	V
Drain Current-Continuous	-ID	12	А
Drain Current-Pulsed Note1	I <sub>DM</sub>	48	А
Junction Temperature	PD	4.5	W
Maximum Power Dissipation	TJ	150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

# **Thermal Characteristics**

Thermal Resistance, Junction-to-Ambient Note2 $R_{\theta JA}$ 27.8 °C/W
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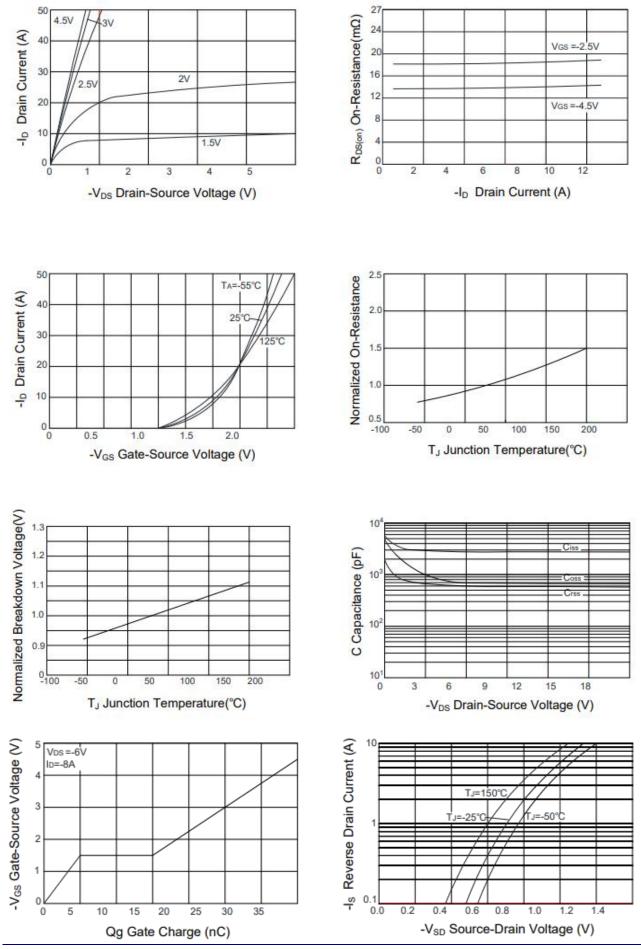
#### Electrical Characteristics(Tc=25°C Unless otherwise specified)

Parameter	Symbols	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	-V <sub>(BR)DSS</sub>	$V_{GS}=0V, I_D=-250 \mu A$	20			V
Zero Gate Voltage Drain Current	-I <sub>DSS</sub>	$V_{DS}$ =-20V, $V_{GS}$ =0V			1	μΑ
Gate-Body Leakage Current	IGSS	V <sub>GS</sub> =±12V,V <sub>DS</sub> =0V			±100	μΑ
Gate Threshold Voltage Note3	-V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_D=-250\mu A$	0.4	0.7	1.0	V
Drain-Source On-Resistance Note3	R <sub>DS(ON)</sub>	$V_{GS} = -4.5 V, I_D = -7A$		17	22	mΩ
		$V_{GS}$ =-2.5V, $I_D$ =-5.6A		22	30	mΩ
Forward Transconductance Note3	g <sub>FS</sub>	$V_{DS} = -5V, I_D = -2A$		18		S
Dynamic Characteristics						
Input Capacitance	Ciss			2000		pF
Output Capacitance	Coss	$V_{DS}$ =-10V, $V_{GS}$ =0V,f=1MHz		242		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			231		pF
Total Gate Charge	Qg			15.3		nC
Gate-Source Charge	Qgs	$V_{DS} = -10V, I_D = -6A, V_{GS} = -4.5V$		2.2		nC
Gate-Drain Charge	Qgd	4.3 V		4.4		nC
Switching Characteristics						
Turn-on Delay Time	t <sub>d(on)</sub>			11		nS
Turn-on Rise Time	tr	$V_{DD} = -10V, I_D = -5A, V_{GNE} =$		35		nS
Turn-off Delay Time	td(off)	$-4.5V,R_{G}=10\Omega$		30		nS
Turn-off Fall Time	tf	<u> </u>		10		nS
Source-Drain Diode Characteristics						
Diode Forward Voltage Note3	-V <sub>SD</sub>	$V_{GS}=0V, I_S=-12A$			1.2	V
Diode Forward Current Note2	-Is				12	А

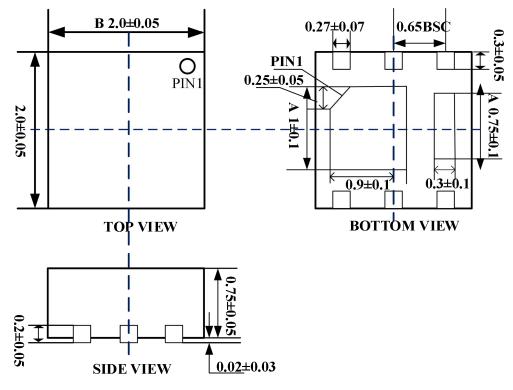
Notes:

- Repetitive Rating: Pulse width limited by maximum junction temperature.
- Surface Mounted on FR4 Board,  $t \leq 10$  sec.
- Pulse Test: Pulse width  $\leq 300 \ \mu$  s, duty cycle  $\leq 2\%$ .

## **Typical Characteristics Curves**



#### Outline Drawing – DFN2x2-6L(Dimensions in mm)



#### **Package Information**

Package Type	Description	Quantity (pcs)	Standard
DFN 2x2-6L	Tape & Reel -7" tape	3000	EIA-481

#### **Contact Information**

TANI website: http://www.tanisemi.com Email:tani@tanisemi.com

For additional information, please contact your local Sales Representative.

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