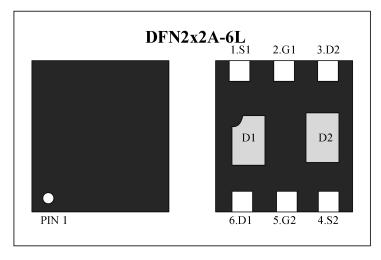


TN05DN20DFA

Dual N-Channel Enhancement Mode Power MOSFET

Features

- V_{DS} = 20V, I_D = 5.5A R_{DS} (on)<28m Ω @ V_{GS} = 4.5V
- Small Surface Mount Package
- Low R_{DS(ON)}
- RoHS Compliant



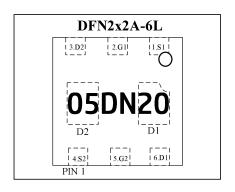
Mechanical Characteristics

- Package:DFN 2x2A-6L
- Packaging: Tape and Reel per EIA 481
- Marking : Making Code

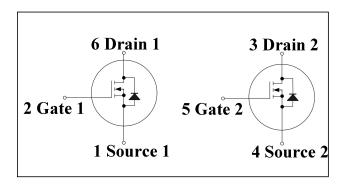
Applications

- Load Switch
- DC/DC Converter

Marking: Making Code



Schematic Diagram



Absolute Maximum Rating(Ratings at 25 °C ambient temperature unless otherwise specified.)

Parameter	Symbols	Value	Unit
Drain-Source Voltage	$V_{ m DS}$	20	V
Gate-Source Voltage	$ m V_{GS}$	±12	V
Drain Current-Continuous	I_D	5.5	A
Drain Current-Pulsed Note1	I_{DM}	18	A
Junction Temperature	P_{D}	1	W
Maximum Power Dissipation	$T_{ m J}$	150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

Thermal Characteristics

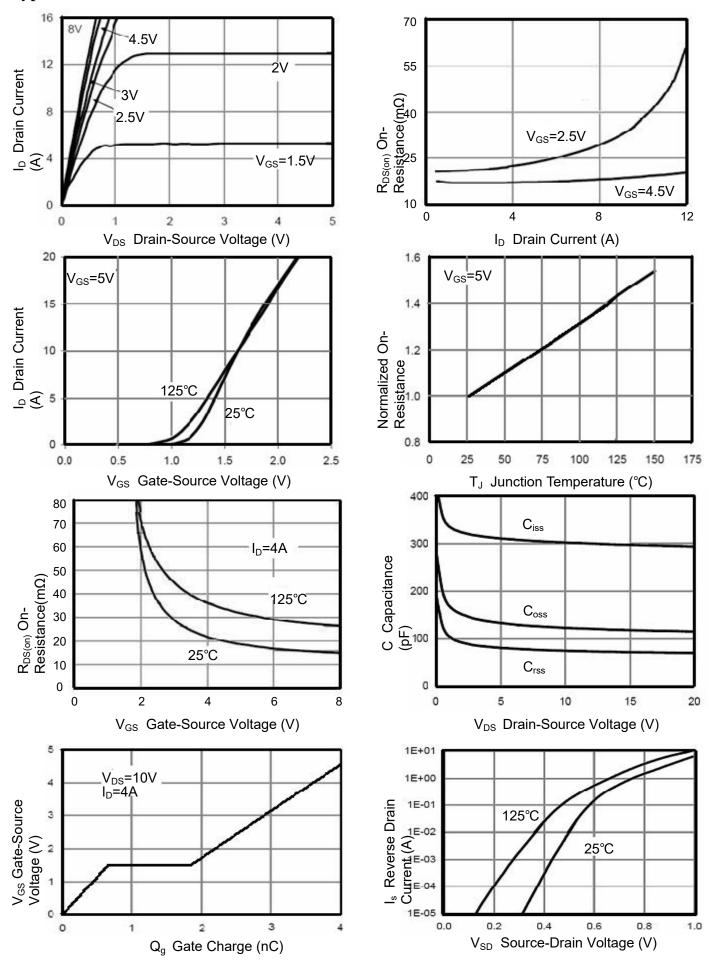
220011	Thermal Resistance, Junction-to-Ambient Note2	$R_{ heta JA}$	125	°C/W
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Electrical Characteristics(Tc=25°C Unless otherwise specified)

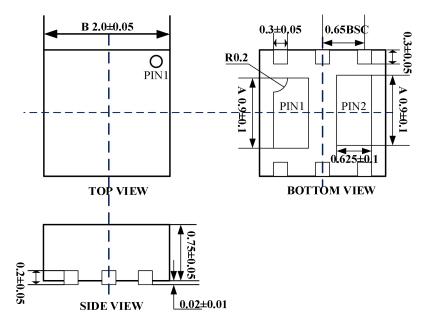
Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static Characteristics					,	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V,I _D =250µA	20			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V,V _{GS} =0V			1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V			±100	nA
Gate Threshold Voltage Note3	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	0.45		1	V
		V _{GS} =4.5V, I _D =4A			28	mΩ
Drain-Source On-Resistance Note3	R _{DS(on)}	V _{GS} =2.5V, I _D =3A			35	mΩ
		V _{GS} =1.8V, I _D =2A			55	mΩ
Forward Transconductance Note3	G FS	V _{DS} =5V,I _D =3A		8		S
Dynamic Characteristics			•		,	
Input Capacitance	C _{iss}	V _{DS} =10V,V _{GS} =0V,f=1MHz		300		pF
Output Capacitance	Coss			120		pF
Reverse Transfer Capacitance	C _{rss}			80		pF
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}			10		nS
Turn-on Rise Time	t _r	V_{DD} =10V, V_{GS} =4.5V I_{D} =3A, R_{GEN} =6 Ω		50		nS
Turn-off Delay Time	t _{d(off)}			17		nS
Turn-off Fall Time	t _f			10		nS
Total Gate Charge	Qg			4.0		nC
Gate-Source Charge	Q _{gs}	$V_{DS} = 10V, V_{GS} = 4.5V$ $I_{D} = 4A$		0.65		nC
Gate-Drain Charge	Q_{gd}	- U -7/		1.2		nC
Source-Drain Diode Characteristics					<u>, </u>	
Diode Forward Voltage Note3	V _{SD}	V _{GS} =0V,I _S =5.5A			1.2	V
Diode Forward Current Note2	Is				5.5	Α

- Notes:
- Repetitive Rating: Pulse width limited by maximum junction temperature.
- Surface Mounted on FR4 Board, $t \leq 10$ sec.
- Pulse Test: Pulse width≤300 µ s, duty cycle≤2%.

Typical Characteristics Curves



Outline Drawing – DFN2x2-6L-0002(Dimensions in mm)



Package Information

Package Type	Description	Quantity (pcs)	Standard
DFN2102-6L-0002	Tape & Reel -7" tape	3000	EIA-481

Contact Information

TANI website: http://www.tanisemi.com Email:tani@tanisemi.com

For additional information, please contact your local Sales Representative.



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