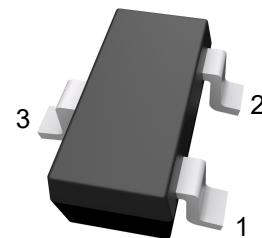


N-Channel Enhancement Mode Power MOSFET
Features

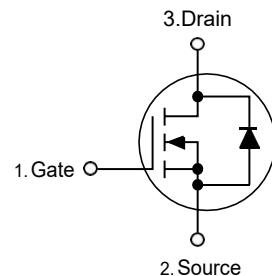
- High density cell design for ultra low $R_{DS(on)}$
- $V_{DS} = 40V, I_D = 5A$
- $R_{DS(on)} < 45m\Omega @ V_{GS} = 10V$

SOT-23-3


1. Gate 2. Source 3. Drain

Application

- Load switch and in PWM applications
- Power management

Marking Code:KJC
Schematic Diagram

Absolute Maximum Ratings

Ratings at 25°C ambient temperature unless otherwise specified.

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	5	A
Drain Current-Pulsed ^{Note1}	I_{DM}	20	A
Maximum Power Dissipation	P_D	1.2	W
Junction Temperature	T_J	150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

Thermal Characteristics

Thermal Resistance, Junction-to-Ambient ^{Note2}	$R_{\theta JA}$	104	°C/W
--	-----------------	-----	------

Electrical Characteristics

(Ta=25°C unless otherwise specified)

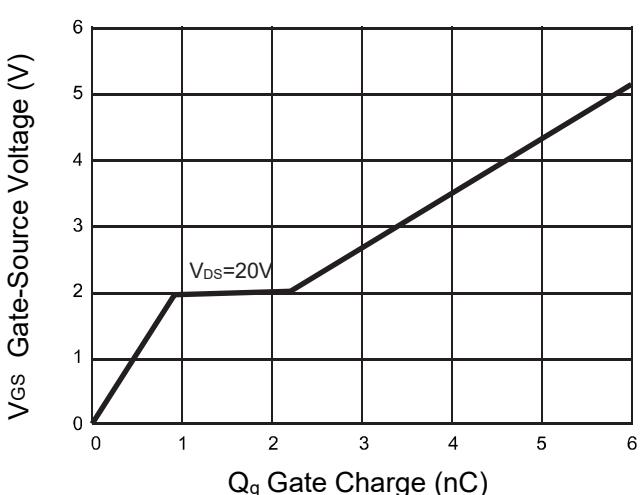
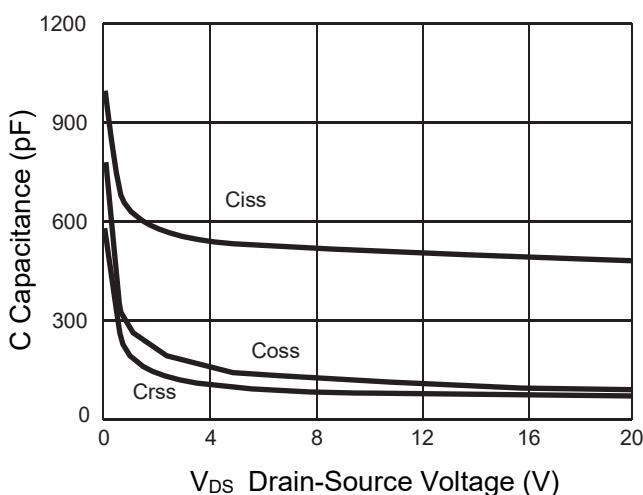
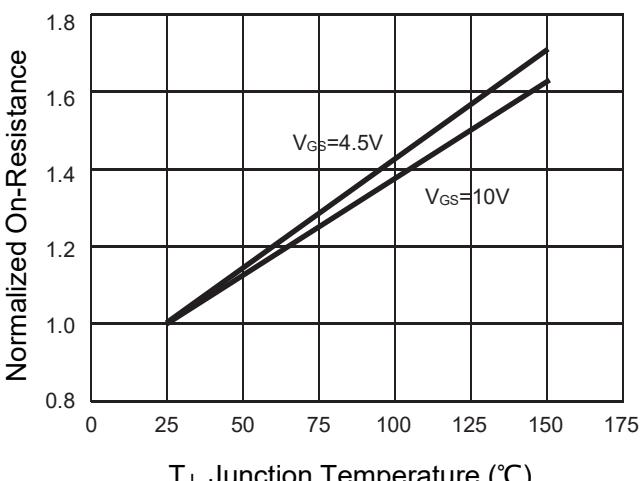
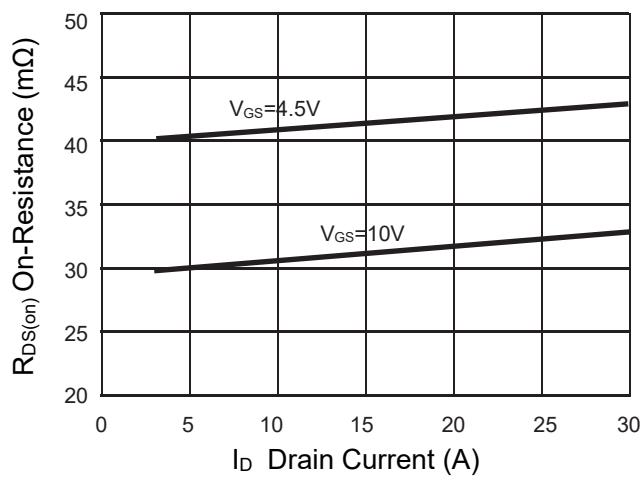
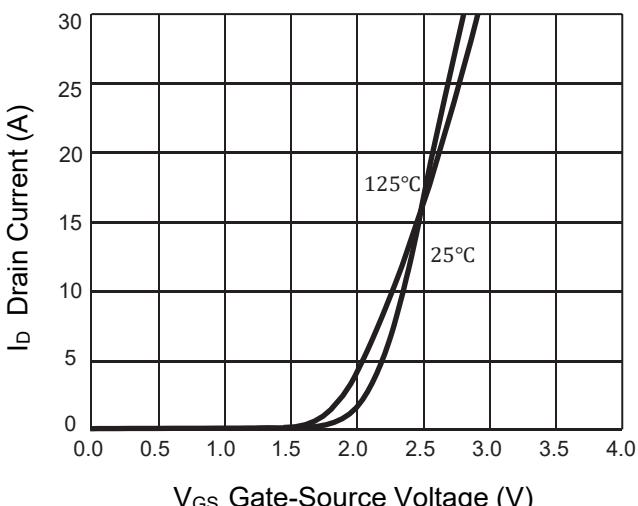
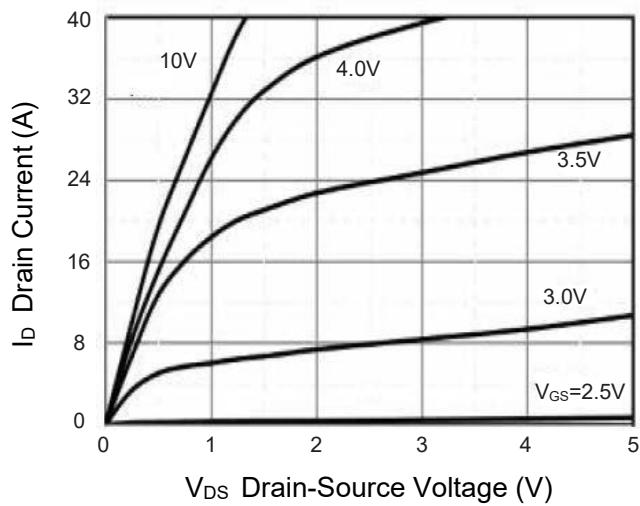
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	40	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V, V _{GS} =0V	--	--	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
Gate Threshold Voltage ^{Note3}	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1	1.5	2.5	V
Drain-Source On-Resistance ^{Note3}	R _{DS(on)}	V _{GS} =10V, I _D =5A	--	30	45	mΩ
		V _{GS} =4.5V, I _D =3A	--	40	60	mΩ
Forward Transconductance ^{Note3}	g _{FS}	V _{DS} =5V, I _D =1A	--	5	--	S
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =20V, V _{GS} =0V, f=1MHz	--	490	--	pF
Output Capacitance	C _{oss}		--	92	--	pF
Reverse Transfer Capacitance	C _{rss}		--	68	--	pF
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}	V _{DD} =20V, R _L =2Ω, V _{GS} =10V, R _{GEN} =3Ω	--	13	--	nS
Turn-on Rise Time	t _r		--	52	--	nS
Turn-off Delay Time	t _{d(off)}		--	17	--	nS
Turn-off Fall Time	t _f		--	10	--	nS
Total Gate Charge	Q _g	V _{DS} =20V, I _D =3.5A, V _{GS} =10V	--	5.2	--	nC
Gate-Source Charge	Q _{gs}		--	0.9	--	nC
Gate-Drain Charge	Q _{gd}		--	1.3	--	nC
Source-Drain Diode Characteristics						
Diode Forward Voltage ^{Note3}	V _{SD}	V _{GS} =0V, I _s =5A	--	--	1.2	V
Diode Forward Current ^{Note2}	I _s		--	--	5	A

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, t ≤ 10 sec.

3. Pulse Test: Pulse width≤300μs, duty cycle≤2%.

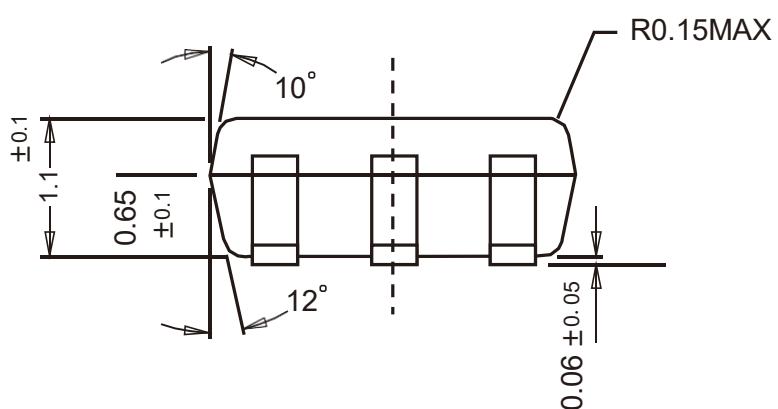
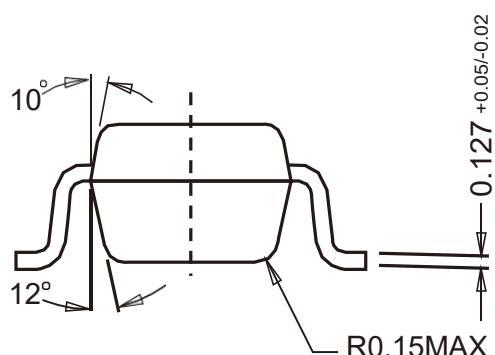
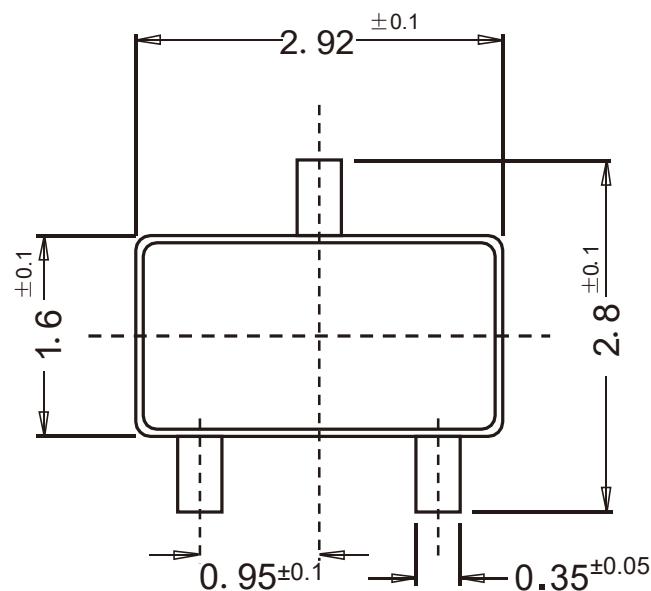
Typical Characteristic Curves



Package Outline

SOT-23-3

Dimensions in mm

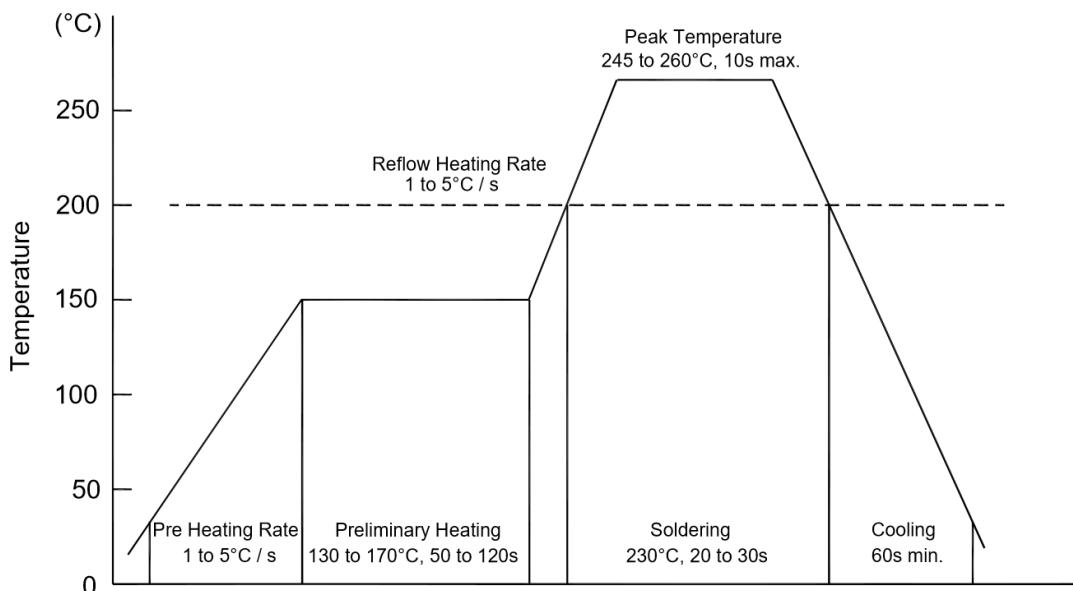


Ordering Information

Device	Package	Shipping
TN05N40SC	SOT-23-3	3,000PCS/Reel&7inches

Conditions of Soldering and Storage

◆ Recommended condition of reflow soldering



Recommended peak temperature is over 245 °C. If peak temperature is below 245 °C, you may adjust the following parameters:

- Time length of peak temperature (longer)
- Time length of soldering (longer)
- Thickness of solder paste (thicker)

◆ Conditions of hand soldering

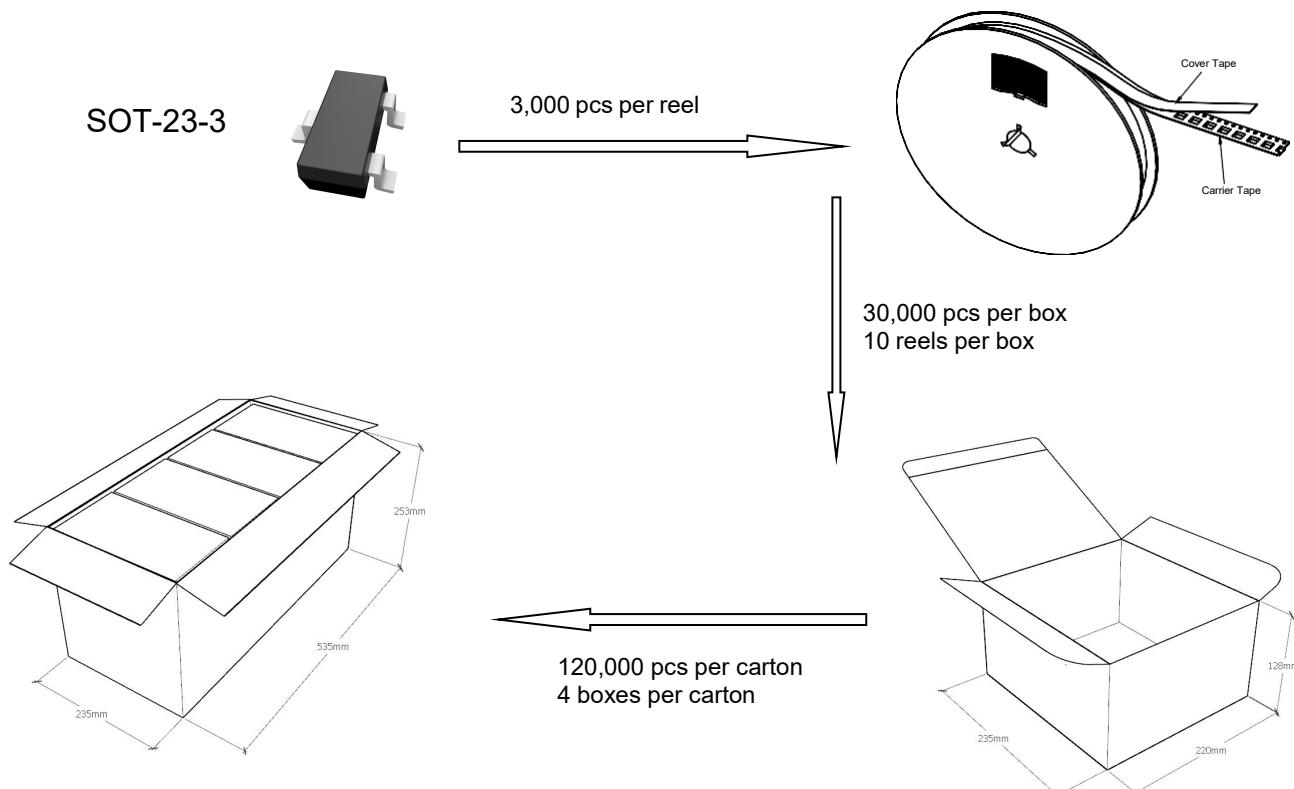
- Temperature: 370 °C
- Time: 3s max.
- Times: one time

◆ Storage conditions

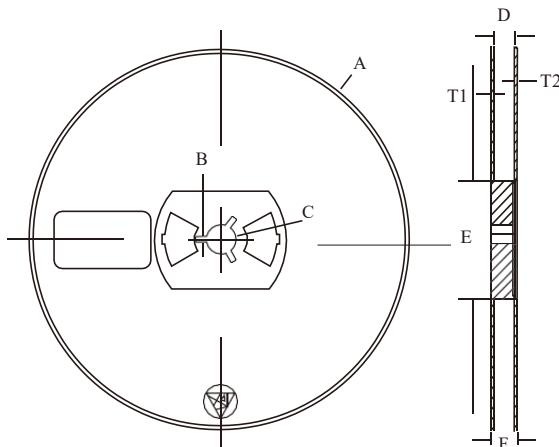
- **Temperature**
5 to 40 °C
- **Humidity**
30 to 80% RH
- **Recommended period**
One year after manufacturing

Package Specifications

- The method of packaging



◆ Embossed tape and reel data



Symbol	Value (unit: mm)
A	$\varnothing 177.8 \pm 1$
B	2.7 ± 0.2
C	$\varnothing 13.5 \pm 0.2$
E	$\varnothing 54.5 \pm 0.2$
F	12.3 ± 0.3
D	$9.6 +2/-0.3$
T1	1.0 ± 0.2
T2	1.2 ± 0.2

Reel (7")

