

IT(RMS)		100A
VDRM/VRRM	BAT100-1200	1200V
	BAT100-1600	1600V
VTM		1.5V

FEATURES

IT(RMS): 100A

VGT: 1.3V

VDRM VRRM:1200V~1600V

High Junction Temperature

Good Commutation Performance

High dV/dt and dI/dt

APPLICATIONS

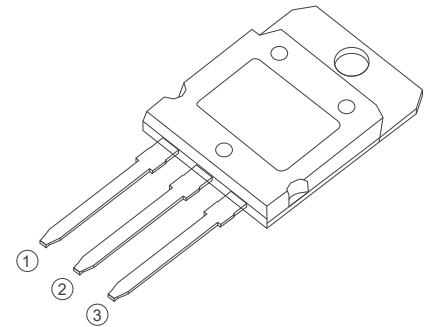
Heater Control

Motor Speed Controller

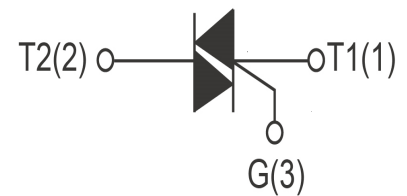
Washing machine

Vacuums

Solid state relay



ITO-247 Insulated



Absolute Maximum Ratings (Tj=25°C unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit
VDRM VRRM	Repetitive Peak Off-State Voltage	BTA100-1200B	1200	V
		BTA100-1600B	1600	
IT(RMS)	R.M.S On-State Current	ITO-247(Ins) Tc=70°C	100	A
ITSM	Surge On-State Current	Tp=20ms	1100	A
I²t	I²t for fusing	Tp=10ms	5500	A²s
PG(AV)	Average Gate Power Dissipation	Tj=125°C	2	W
IGM	Peak Gate Current	Tj=125°C	8	A
PGM	Peak Gate power		10	W
Tj	Operating Junction Temperature		~40~125	°C
TSTG	Storage Temperature		~40~150	

Electrical Characteristics (T_j=25°C unless otherwise specified)

Symbol	Parameter	Test Conditions	Value	Unit
IDRM	Repetitive Peak Off-State Current	T _j =25°C	20	uA
		T _j =125°C	12	mA
IRRM	Repetitive Peak Reverse Current	T _j =25°C	20	uA
		T _j =125°C	12	mA
VTM	Forward "on" voltage	I _T =80A, t _p =380us, T _j =25°C	≤1.5	V
VGT	Gate trigger voltage	V _D =12V, R _L =33Ω	≤1.3	V
di/dt	V _D =2/3V _{DRM} Gate Open, T _j =125°C I,I,II,III,IV	F=100Hz, I _G =2xI _{GT} , t _r ≤100ns	100	A/us
IGT	Gate trigger current	I,I,II,III V _D =12V, R _L =33Ω	≤50	mA
IH	Holding current	I _T =100mA	≤100	
VGD	Gate non-trigger voltage	V _D =V _{DRM} , T _J =125°C, R _L =3.3KΩ	0.2	V
dv/dt	Critical-rate of rise of commutation voltage	T _J =125°C, V _D =2/3V _{DRM} , Gate open circuit	≥1500	V/us
Rth(j-c)	Thermal resistance	Junction to ase	ITO-247(Ins)	°C/W

FIG1

Maximum power dissipation versus RMS on-state current

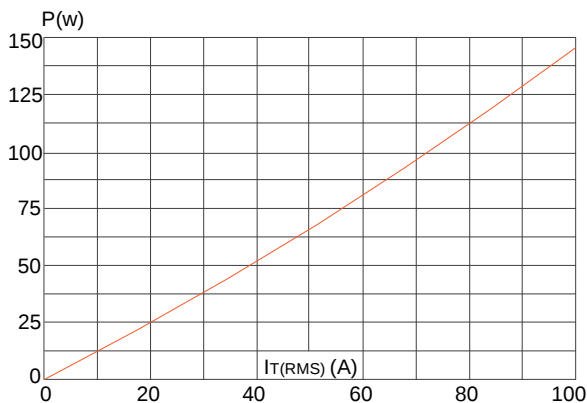


FIG2

RMS on-state current versus case temperature

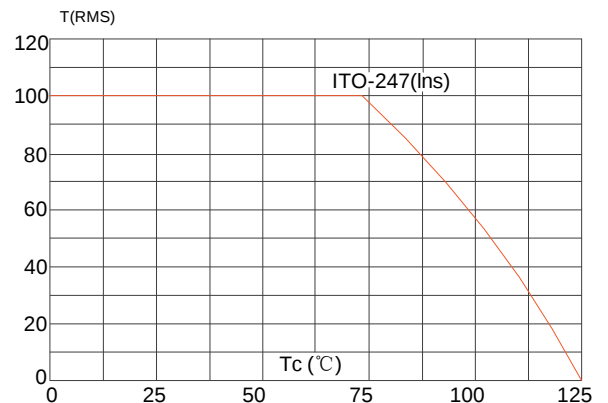


FIG3

Surge peak on-state current versus number of cycles

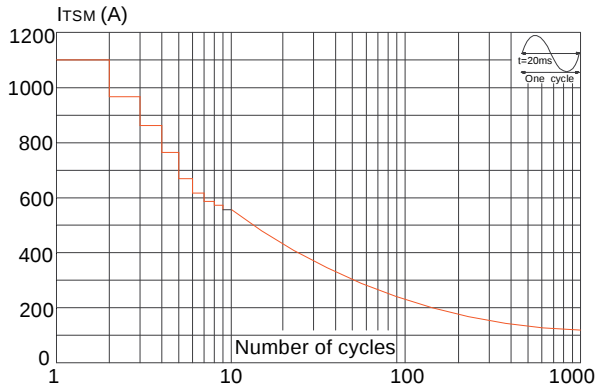


FIG4

On-state characteristics (maximum values)

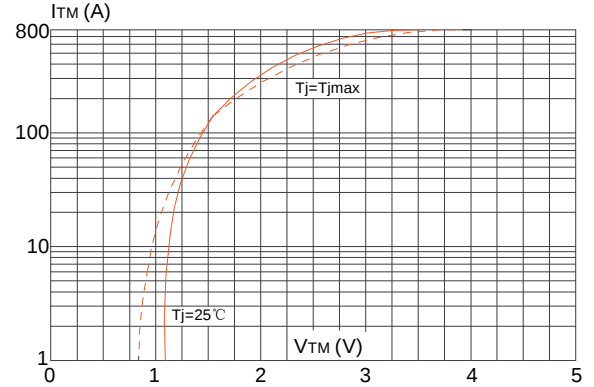


FIG5

Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20ms$, and corresponding value of $I^2 t$ ($di/dt < 100A/\mu s$)

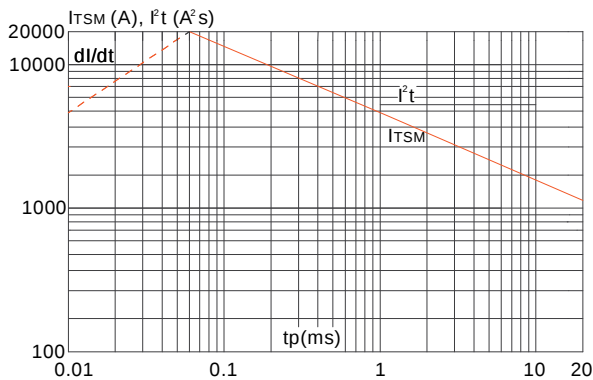
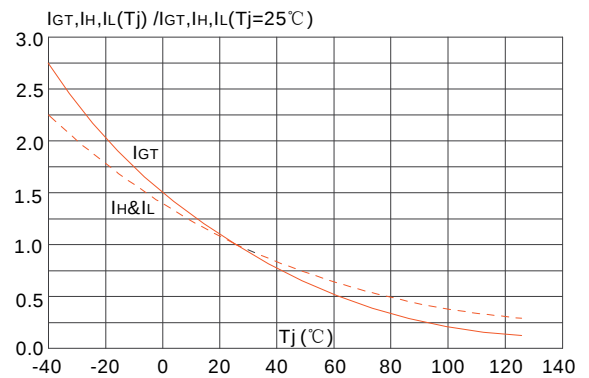
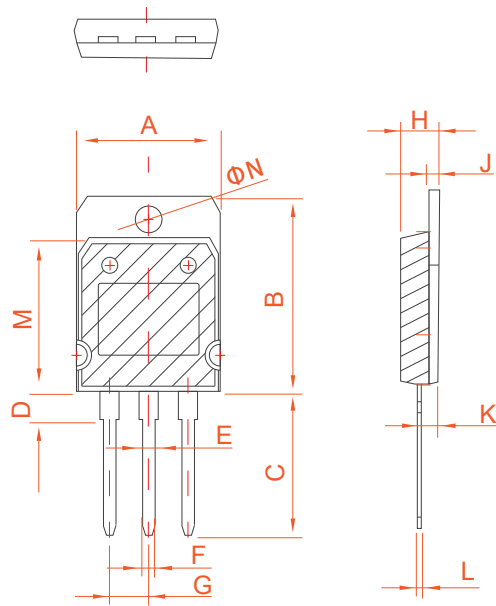


FIG6

FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



PACKAGE MECHANICAL DATA



ITO-247 (Ins)

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	19.7	19.9	20.1	0.776	0.783	0.791
B	26.9	27.1	27.3	1.059	1.067	1.075
C	19.4	19.9	20.4	0.764	0.783	0.803
D	3.80	3.90	4.00	0.150	0.154	0.157
E	2.56	2.66	2.76	0.101	0.105	0.109
F	1.66	1.76	1.86	0.065	0.069	0.073
G		5.45			0.215	
H	5.05	5.10	5.50	0.199	0.201	0.217
J	1.45	1.50	1.55	0.057	0.059	0.061
K	2.20	2.30	2.40	0.087	0.091	0.094
L	0.60	0.70	0.80	0.024	0.028	0.031
M	21.2	21.3	21.4	0.835	0.839	0.843
N	3.20	3.30	3.40	0.126	0.130	0.134